

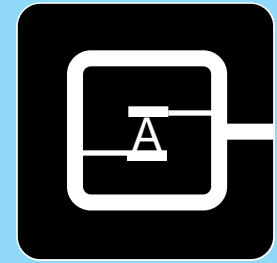
IEEE

CIRCUITS AND SYSTEMS

NEWSLETTER SOCIETY

Volume 8, Number 3, September 1997

ISSN 1049-3654



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DECEMBER



JUNE

MARCH

IEEE Circuits and Systems Society Newsletter

Newsletter Homepage—<http://www.nd.edu/~stjoseph/newscas>

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Piscataway, NJ 08855-1331, USA
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Months of Publication

March
June
September
December

Newsletter Deadlines

Articles for the CAS Newsletter issues must be received by the Editor by the following dates:

Issue	Due Date
March	February 1
June	May 1
September	August 1
December	November 1

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IEEE Circuits and Systems Society Newsletter is published quarterly by the Circuits and Systems Society of the Institute of Electrical and Electronics Engineers, Inc., 345 East 47th Street, New York, NY 10017. Four dollars per member per year (included in Society fee) for each member of the Circuits and Systems Society. Printed in U.S.A. Periodicals Postage Paid at New York, NY, and at additional mailing offices. **Postmaster:** Send address changes to IEEE Circuits and Systems Society Newsletter, Attn: Change of Address, IEEE, 445 Hoes Lane, Piscataway, NJ 08855-1331.

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J. Abraham, 1997 *ICCD General Chairman*
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P. Thajchayapong, 1998
APCCAS General Chairman
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BOARD OF GOVERNORS' CORNER

A REPORT FROM HONG KONG

Barbara Wehner, Admin. Asst.
John Choma, President

The following article is an overview of the Board of Governors meeting held during ISCAS'97 in Hong Kong. —Editor

John Choma, President of the IEEE Circuits and Systems Society, called the Board of Governors (BOG) meeting to order at the Hong Kong Convention & Exhibition Centre, Harbour Road, Wanchai, Hong Kong. After roll call and various other housekeeping items were taken care of, the Executive Summary Reports were given.

President's Report

To begin the President's Report, John Choma emphasized again, as he has since the beginning of his term, that he has chosen education as the theme of his presidency. He indicated that this issue is very dear to him personally but also feels that the Society, in general, must address it as well and build upon it. To that end, he has asked Chris Toumazou, VP-Technical Activities, to put together a short course program which is already in the planning stages. Toumazou would comment on this program later.

Choma briefly commented on the decrease in overall CAS membership. There is a continuing negative slope of concern to all, especially with the Solid-State Circuits Council becoming its own society. Choma indicated that Bell Shenoï has come up with several membership development proposals which he would also present later during the meeting.

Continuing, Choma indicated that there are several collaborative activities in discussion stages with other societies. A very fruitful meeting has already taken place with the Communications Society in San Francisco in April which Choma, Rui de Figueiredo (president-elect) and Yih-Fang Huang (VP-Publications) attended.

Finally, Choma indicated the need for the Society to expand its horizons in global ways. It is due to this need, that he and president-elect, Rui de Figueiredo, have decided to form a President's Advisory Council

(PAC). There is also a subcommittee of PAC concerned with international affairs, an area of great importance.

In summary, Choma emphasized that the Circuits and Systems Society must attain greater visibility, provide better services, and expand its horizons in the future.

Publications Division

Yih-Fang Huang, vice president for publications, brought several action items before the Board concerning the Transactions. Due to increased submissions and a need to reduce the backlog of papers to be published, a motion was presented and approved to increase the page budget for *T-CAS I*, *T-CAS II*, *T-CSVT* and *T-VLSI*. Motions were also passed to increase the frequency of *T-VLSI* from four to six issues annually, and increase *T-CSVT* by two issues, from six to eight annually. In order to allow for these extra issues, motions were passed to increase the operational budget for *T-CSVT* in the amount of \$31.5K for 1998, and increase the operation budget for the editor of *T-VLSI* to \$30.5K—reviewed after one year. John Choma, with Huang's approval, recommended tabling a *Circuits and Devices Magazine* action item, suggesting the Publications Division continue further discussions with Ron Waynant, current editor-in-chief of the magazine.

Huang then noted that he had attended the Panel of Editors meeting at the IEEE Headquarters on April 11–12. Many topics of interest were covered but his concerns are mostly with the review process, timeliness of manuscript processing, and high quality papers on timely technical topics. Huang also revealed that IEEE surveys indicate that publications are a very important component of a society's reputation. A discussion of these topics then ensued.

Finally, as an information item, Choma indicated that the Communications Society has invited CAS to collaborate on a special issue of the *Communications Transactions*. This would definitely be a bilateral move.

BOG Meeting Attendees

Society Officers:

John Choma, *President*
Rui J. P. de Figueiredo,
President-Elect
Hari Reddy, *VP-Conferences*
Yih-Fang Huang,
VP-Publications
Franco Maloberti, *VP-Region 8*
Paulo S. R. Diniz, *VP-Region 9*
Graham Hellestrand,
VP-Region 10
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Magdy Bayoumi
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Ibrahim Hajj
George Moschytz
Peter Pirsch
Bing Sheu

Transactions Editors:

Josef A. Nossek, *T-CAS I*
Ming-Ting Sun, *T-CSVT*
(*Outgoing Editor*)
Ya-Qin Zhang, *T-CSVT*
(*Incoming Editor*)

Others:

Wai-Kai Chen, *Chair*,
Nominations Committee
Eby Friedman, *Variety of*
CAS positions
Ming L. Liou, *Co-chair*,
ISCAS'97
Bell A. Shenoï, *Chair*,
Distinguished Lecture Program;
Assoc. Editor, T-CAS II
David Skellern, *Visitor*
Thanos Stouraitis, *Visitor*
Barbara Wehner,
CAS Administrative Assistant

... continued on Page 10

AWARDS NOMINATIONS 1998

It is once again time for nominations for the Circuits and Systems Society Awards, either in the category of a Society Achievement Award or an Outstanding Paper Award. This year, to simplify the procedure, all nominations are to be sent to one person. **Please use the forms on the following page and send your nomination(s) to Professor John Choma, Jr., University of Southern California, Department of Electrical Engineering-Electrophysics, University Park; Mail Code: 0271, Los Angeles, CA 90089-0271. Ph: (213) 740-4692; Fax: (626) 915-0944; E-mail: johnc@mizar.usc.edu**

◇◇ Education Award ◇◇

Purpose: To honor a person with outstanding contributions to education in a field within the scope of the CAS Society as documented by publications of textbooks, research supervision of graduate and undergraduate students, development of short courses and participation in adult education. The award is based on general quality and originality of contributions and continuity of effort. Anyone who is a member of the CAS Society is eligible. **Prize:** Plaque and \$500 check.

◇◇ Technical Achievement Award ◇◇

Purpose: To honor a person with outstanding technical contributions over a period of years within the scope of the CAS Society as documented by publications (including patents). The award is based on the general quality and originality of contributions and continuity of effort. Anyone who is a member of the CAS Society is eligible. **Prize:** Plaque and \$500 check.

◇◇ Mac Van Valkenburg Award ◇◇

Purpose: To honor a person with outstanding technical contributions in a field within the scope of the CAS Society and outstanding leadership in the field. The award is based on quality and significance of contribution and continuity of technical leadership. Anyone who is a member of the CAS Society is eligible. **Prize:** Plaque and \$1,000 Check.

◇◇ Chapter-of-the-Year Award ◇◇

Purpose: To recognize the CAS Society Chapter with the best yearly activities. The award is based on best yearly activities in the categories of Chapter-sponsored technical activities, increase in membership and participation in BOG meetings. Anyone who is a member of the CAS Society Chapters is eligible. **Prize:** Certificates to Chapter Officers.

◇◇ Meritorious Service Award ◇◇

Purpose: To honor a person with outstanding long-term service to the welfare of the CAS Society. The award is based on dedication, effort and contributions. Anyone who is a member of the CAS Society is eligible. **Prize:** Plaque and \$500 check.

◇◇ Guillemin-Cauer Award ◇◇

Purpose: To recognize the best paper published in the *IEEE Transactions on Circuits and Systems*. The award is based on general quality, originality, contributions, subject matter and timeliness. Anyone who is an author of a paper published in the *IEEE Transactions on Circuits and Systems* during the two calendar years preceding the award is eligible. **Prize:** Certificate and \$250 check for each author (maximum of \$1,000 per award).

◇◇ Darlington Award ◇◇

Purpose: To recognize the best paper bridging the gap between theory and practice published in the *IEEE Transactions on Circuits and Systems*. The award is based on general quality, originality, contributions, subject matter and timeliness. Anyone who is an author of papers bridging the gap between theory and practice published in the *IEEE Transactions on Circuits and Systems* during the two calendar years preceding the award is eligible. **Prize:** Certificate and \$250 check for each author (maximum of \$1,000 per award).

◇◇ CAD Transactions Best Paper Award ◇◇

Purpose: To recognize the best paper published in the *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems*. The award is based on general quality, originality, contributions, subject matter and timeliness. Anyone who is an author of a paper published in the *IEEE Transactions on Computer-Aided Design of Integrated Circuits and Systems* during the two calendar years preceding the award is eligible. **Prize:** Certificate and \$250 check for each author (maximum of \$1,000 per award).

◇◇ VLSI Transactions Best Paper Award ◇◇

Purpose: To recognize the best paper published in the *IEEE Transactions on Circuits and Systems for Very Large Scale Integration (VLSI) Systems*. The award is based on general quality, originality, contributions, subject matter and timeliness. Anyone who is an author of a paper published in the *IEEE Transactions on Circuits and Systems for VLSI Systems* during the two calendar years preceding the award is eligible. **Prize:** Certificate and \$250 check for each author (maximum of \$1,000 per award).

◇◇ CSVT Transactions Best Paper Award ◇◇

Purpose: To recognize the best paper published in the *IEEE Transactions on Circuits and Systems for Video Technology*. The award is based on general quality, originality, contributions, subject matter and timeliness. Anyone who is an author of papers published in the *IEEE Transactions on Circuits and Systems for Video Technology* during the two calendar years preceding the award is eligible. **Prize:** Certificate and \$250 check for each author (maximum of \$1,000 per award).

◇◇ Outstanding Young Author Award ◇◇

Purpose: To honor an especially meritorious paper published in any one of the CAS Society's Transactions whose author at the date of submission is less than 30 years of age. The award is based on general quality, originality, contributions, subject matter and timeliness. Anyone who is an author of papers published in any one of the CAS Society Transactions during the two calendar years preceding the award, who at the date of submission of the paper shall be less than 30 years of age is eligible. **Prize:** Certificate and \$250 check for each author (maximum of \$1,000 per award).

IEEE Circuits and Systems Society 1998 Outstanding Paper Award Nomination

Outstanding Young Author Award—CAD Transactions Best Paper Award
Guillemin-Cauer Award—CSVT Transactions Best Paper Award
Darlington Award—VLSI Transactions Best Paper Award

Paper Award Recommended: _____

Paper Title: _____

Paper Authors: _____

Paper Listing:

Name of Transactions: _____

Month: _____ Year: _____ Pages: _____

Nominator:

Name: _____ Tel (day): _____ Tel (home-opt.): _____

Address: _____ Fax: _____ E-mail: _____

Basis for Nomination:

Please give the reasons you believe this paper is deserving of the outstanding paper award. Judging is based upon general quality, originality, contribution, subject matter, and timeliness. Continue on additional page(s).

IEEE Circuits and Systems Society 1998 Society/Achievement Award Nomination

Education Award—Mac Van Valkenburg Award—Meritorious Service Award
Chapter-of-the-Year Award—Technical Achievement Award

Name of Award: _____ **Date:** _____

Nominee:

Name: _____

Address: _____

Present Employment Position(s): _____

Highest Degree Attained: _____

Birthdate: _____

Telephone (day): _____

Nominator:

Name: _____

Address: _____

Telephone (day): _____

Tel (home-opt.): _____

Fax: _____

E-mail: _____

1. Proposed Citation:

Provide a brief statement, not exceeding 100 words, giving the major accomplishments for which the award is being made. This will be used if the nominee is selected as the awardee. Continue on separate page(s).

2. Basis for Nomination:

Prepare a statement not exceeding 750 words on why the candidate is being nominated for the award. This statement should then be followed by the record of accomplishments of the candidate as an educator, as a researcher and/or as an administrator, as appropriate. Continue on separate page(s).

3. Curriculum Vitae:

Include degrees earned (list universities and granting dates); other postgraduate study; record of all positions held (chronologically starting with the most recent position and outlining the duties for each); IEEE activities and offices; other society memberships and offices; awards, honors, publications, inventions and other relevant contributions. Continue on separate page(s).

4. Publications:

List all books, book chapters, journal papers and patents published. If appropriate, include these in the vitae. Select no more than 10 of the most important publications stating the engineering significance of each. Continue on separate page(s).

5. References:

No more than five brief supporting letters from colleagues (and former students for the CAS Society Education Award) should be included with each award nomination. List the names of the references on the nomination form. The reference letters can either be collected by the nominator and forwarded unopened to John Choma or the references can be instructed to forward their recommendations directly to Professor Choma. All reference letters must be received by the due date of the nominations, February 1, 1998.

IEEE CAS FELLOW PROFILES 1997

Mitsumasa Koyanagi

For the invention of the stacked capacitor DRAM cell.



Mitsumasa
Koyanagi

Mitsumasa Koyanagi received the B.S. degree in electrical engineering from Muroran Institute of Technology, Hokkaido, in 1969 and the M.S. and Ph.D. degrees in electronic engineering from Tohoku University, Sendai, in 1971 and 1974, respectively. In 1974 he joined Hitachi Central Research Laboratory, where he engaged in the research of high density DRAM process and device technology. In 1985 he joined Xerox Palo Alto Research Center in California, where he engaged in the research of submicron CMOS technology, analog/digital LSI design and thin-film transistor (TFT) technology. In 1988 he became a professor in Research Center for Integrated Systems, Hiroshima University. Since 1994 he has been professor in Intelligent System Design Laboratory, Department of Machine Intelligence and System Engineering, Tohoku University.

His current interests are sub-0.1 μ m

MOS device physics and device modeling, cryo-electronics, low-power/low voltage circuits, TFT/LCD technology, optoelectronic circuit and optical interconnection, three-dimensional (3D) integration technology and special purpose parallel processing system including real-time image processors. He has pioneered several new process and device technologies for dynamic random access memory (DRAM). Most well known is his invention of Stacked Capacitor Cell (STC) with three-dimensional structure. Stacked Capacitor Cell has been widely used in mass-production of 4Mbit DRAM and 16Mbit DRAM and is going to be employed in 64Mbit, 256Mbit, and 1Gbit DRAMs. He has published more than 150 papers. He received the Ohkouchi Award in 1991, the SSDM Award (International Conference on Solid State Devices and Materials) in 1994, and IEEE Cleo Brunetti Award in 1996.

Donald W. Bouldin

For contributions to the design of special-purpose architectures using VLSI processors.



Donald W.
Bouldin

Don Bouldin is professor of electrical engineering at the University of Tennessee, Knoxville. He received the B.E. degree from Vanderbilt University in 1967, the M.S.E.E. degree from Georgia Tech in 1968, and the Ph.D. degree in electrical engineering from Vanderbilt in 1975 when he joined the faculty at the University of Tennessee. In addition to teaching VLSI systems design classes, he has served as an instructor for eight faculty enhancement short courses on VLSI and FPGAs sponsored by the NSF and presented several tutorials and seminars in Mexico, India, Canada, Thailand, and South Korea.

During his career, Dr. Bouldin has contributed to the development of special-purpose VLSI architectures for coincidence detection, symbol matching, sorting, providing

communications for coprocessor circuits, solving Navier-Stokes equations, and executing rule-based programs in parallel. Each of these was implemented using VLSI technology, fabricated, tested and, in the case of coincidence detection, commercialized.

He was the editor-in-chief of the *IEEE Transactions on VLSI Systems* during 1995-1996. For more than eight years, he has served as special section editor of the "VLSI Designer's Interface" column for *IEEE Circuits & Devices Magazine*. He was the technical program chairman for ISCAS'96. He has authored more than 140 publications and been the principal investigator for over four million dollars of sponsored research, primarily with the nearby Oak Ridge National Laboratory, NSF, and DARPA.

CIRCUITS AND SYSTEMS SOCIETY MEMBERS

Richard V. Snyder

For contributions to the development of high power miniature stopband filters and extremely wideband bandpass filters for microwave applications.

Dr. Richard V. Snyder is the president and founder of RS Microwave, a well-known 16 year old manufacturer of RF and Microwave filters. He is the author of over 50 papers on the subject of filters and couplers, as well as holder of 13 patents. He received the B.S., M.S. and Ph.D. degrees from Loyola-Marymount, USC and PINY. His current research areas include electromagnetic simulation as applied to filters and networks, dielectric resonators and active filter networks.

Dr. Snyder served the IEEE as North Jersey Section chairman and as a 14 year chapter chairman for the MTT and AP Societies. He has twice received the Region 1 award. A reviewer for the *IEEE Transactions on Microwave Theory and Techniques*, the *IEEE Microwave and Guided Wave Letters*, and other IEEE and MTT publications, Dr. Snyder teaches filter and network courses as an adjunct professor at the New Jersey Institute of Technology. His professional involvement also includes MTT-ADCOM special assignments and various MTT Chapter lectures on the subject of filters and networks.

Dr. Snyder is the recipient of the best paper award at the 1991 MTT-S Symposium. For the last two years, he served as Standards chairman for the MTT ADCOM. Currently serving as co-chairman of MTT-8 (the Microwave Theory and Techniques Society Technical Committee charged with overseeing filters and networks) and co-chairman of the co-located MTT-AP Symposium to be held in Philadelphia in 2003, he also has served the North Jersey Section as METSAC chairman and as an organizer of tutorial sessions for Electro.

Dr. Snyder was a research engineer at ITT-Gilfillan from 1965–1969, working on component development, and chief engineer for Merrimac Industries from 1969–1972. As vice-president of FEL from 1972–1977, he ran the Microwave Division of Frequency Engineering Labs, Farmingdale, New Jersey and was chief engineer for Premier Microwave, Portchester, New York from 1977–1981.

Dr. Snyder's contributions have led to improved performance and decreased size for communication and electronic countermeasure systems.



Richard V.
Snyder

Johan Hendrik Huijsing

For contributions to the design and analysis of analog integrated circuits.

Johan H. Huijsing received the M.S. degree in electrical engineering from the Delft University of Technology, Delft, the Netherlands in 1969, and the Ph.D. degree from the same University in 1981.

He has been involved in teaching and research in the field of electronic instrumentation in the Faculty of Electrical Engineering of the Delft University of Technology since 1969, where he became a full professor in the chair of electronic instrumentation in 1990. From 1982 through 1983 he was a senior scientist at Philips Research Labs in Sunnyvale, California. Since 1983 he has been a consult-

ant for Philips, Sunnyvale.

The research work of Dr. Huijsing particularly contributed to the systematic analysis and design of operational amplifiers and integrated smart sensors. He is author or co-author of 150 scientific papers, 20 patents and four books, and co-editor of five books.

He is initiator and co-chairman of the International Workshop on Advances in Analog Circuit Design, which has been held annually since 1992. He is chairman of the biennial national Workshop on Sensor Technology, since 1991, and chairman of the Dutch STW Platform on Sensor Technology.



Johan Hendrik
Huijsing

IEEE CAS FELLOW PROFILES 1997

Zong Sha

For technical leadership in radiowave-propagation research, practice, and education.



Zong
Sha

Zong Sha graduated from the physics department of Wuhan University in 1949. He worked in the Research Institute of Electronics from 1949 to 1956 as an engineer responsible for design of electronic equipment. In 1956 Zong Sha transferred to the China Research Institute of Radiowave Propagation (CRIRP) and began his research career of more than 40 years in the field of radiowave propagations and electromagnetic engineering projects. His most significant contribution has been to provide what has literally been a professional lifetime of technical leadership to radiowave-propagation research and practical applications in China. The CRIRP has primary responsibility for all radiowave-propagation research throughout China. Beginning as the leader of a group and progressing to the director general of the whole CRIRP, Mr. Sha had direct technical involve-

ment in many accomplishments of the CRIRP.

Moreover, as chief engineer for CRIRP from 1972 to 1985, he determined both the general research directions and specific projects to be carried out in radiowave research for essentially all of China. He did this during a period when there was almost no technical contact with the scientific community outside of China. Furthermore, for at least part of this time, scientific research and engineering were condemned. Since 1985, Mr. Sha's leadership of radio science and electrical engineering in China has continued through his positions as general secretary of China's International Union of Radio Science (URSI) National Committee, of the (only) IEEE Section for China (Beijing Section), and, from 1982, as executive director and deputy secretary general of the Chinese Institute of Electronics.

Hsiao-Dong Chiang

For contributions to the direct methods to power systems stability analysis and to the development of nonlinear systems.



Hsiao-Dong
Chiang

Hsiao-Dong Chiang received the B.S. degree in 1979, the M.S. degree in 1981 in electrical engineering from the National Taiwan University, Taipei, Taiwan, and the Ph.D. degree in 1986 in electrical engineering and computer sciences from the University of California at Berkeley.

He is currently associate professor of electrical engineering at Cornell University, Cornell, New York. He was associate editor of the *IEEE Transactions on CAS* from 1990 to 1991 and associate editor for "Express Letters" of the *IEEE Transactions on CAS: Part I*, from 1993 to 1995. His research interests include the development of theoretical foundations and analytical tools for the analysis and control of physical systems, and the use of this work to achieve valuable and practical results. He developed the theory-

based BCU method, which has received considerable attention world-wide, and has been applied by researchers and engineers to several practical power system problems. This method, recently awarded a U.S. patent, distinguishes itself in that it is now being used in the power industry. Dr. Chiang has also developed a comprehensive theory for stability regions and presented optimal schemes for estimating the stability regions of general nonlinear systems.

Dr. Chiang received the Engineering Research Initiation Award in 1988 and the Presidential Young Investigator Award in 1989, both from the National Science Foundation. In 1990, he was selected by a Cornell Merrill Presidential Scholar as the faculty member who had the most influence on that student's education at Cornell.

CIRCUITS AND SYSTEMS SOCIETY MEMBERS

Robert Piloty

For contributions to and leadership in the development of hardware design languages, of microelectronic CAD systems, and of early computers.

Robert Piloty obtained the Diploma and Doctor's degrees in electrical engineering from the Technische Hochschule München in 1947 and 1949, respectively. In 1950, as assistant professor, he initiated the development of the PERM, a large parallel computer in vacuum technology, and directed it until its commissioning to service in 1956. From 1956 to 1964 he worked in industry (IBM, ITT) mainly in the development of airline reservation systems. Since 1964 he is full professor in electrical engineering and computer science at the Technical University of Darmstadt, since 1990 as an emeritus. There his main research interests fell in the area of hardware description languages and microelectronic CAD systems. He initiated and chaired the IEEE working group on CONLAN, a universal multilevel HDL, and served as principal author of the final CONLAN Report. In the area of CAD sys-

tems he developed with his students the CAD database system IREEN, one of the first of its kind, to serve as the core of an open, multitool CAD system.

Robert Piloty has published numerous papers and two books. He received a number of awards and honors for his achievements in research and for his public services in fostering informatics in the domain of international professional societies and of the universities of his country, such as the IFIP Silvercore, the Distinguished Service Cross First Class of the Federal Republic of Germany, the Konrad Zuse Medal of the German Informatics Society, and membership in the European Academy of Science and Arts. Robert Piloty served also in numerous functions within the International Federation of Information Processing Societies (IFIP) and the German Informatics Society of which he is one of its founding members.



Robert
Piloty

Robert C. Smythe

For contributions to the theory and technology of crystal filters, resonators, and monolithic filters.

Robert C. Smythe received the B.A. degree in 1952 and the B.S. degree in electrical engineering in 1953, both from Rice University, Houston, Texas. He received the M.S.E. degree in electrical engineering from the University of Florida in 1957. Since 1965, Mr. Smythe has been with Piezo Technology, Inc. He has been senior vice-president of research and engineering, since 1987; from 1970 to 1987 he was vice-president of engineering, and was vice-president of research from 1967 to 1970. He has been active in frequency control since 1960.

Mr. Smythe is the author or co-author of

over sixty papers pertaining to monolithic and discrete quartz crystal filters and resonators, precision measurements, and SAW resonators and filters. He has contributed to the *IEEE Transactions on Ultrasonics, Ferroelectrics and Frequency Control*, the *Proceedings of the IEEE*, the *Proceedings of the Frequency Control Symposium*, and a number of other publications. He holds three patents and has contributed two book chapters. He has served on several IEEE standards committees. In 1991, Mr. Smythe was the recipient of the Walter G. Caday Award of the UFFC.



Robert C.
Smythe

BOARD OF GOVERNORS' CORNER

BOG Report . . . continued from Page 3

Conference Division

Hari Reddy, vice president for conferences, presented a detailed report on the status of the Conference Division. He then introduced Tony Ng, ISCAS'97 co-chair, who provided some conference statistics as of June 8, 1997. A more detailed report would be presented at the ISCAS Steering Committee meeting on Friday, June 13, 1997.

Before continuing with the conference budget, Reddy took time to acknowledge and thank all of the members of the Conference Division for their many contributions. The members of the Conference Division are Giovanni De Micheli, Geert De Veirman, Al Dunlop, Ibrahim Hajj and Mani Soma.

As indicated in the Conference Division Report, the projected Conference Division Budget for 1998 is:

- (i) Budgeted income = \$2,294,439
- (ii) Budgeted expenses = \$2,126,014
- (iii) Grants = \$45,600
- (iv) Total expense budgeted =
Budgeted expense + Grants = \$2,171,614
- (v) 1998 net budgeted = item (i) - item (iv) = \$122,825

The largest surplus is with DAC. Reddy and the members of the Conference Division are exploring various possibilities of how to spend the DAC surplus and promote DA activities within CAS. Al Dunlop, BOG member of the Conference Division, emphasized that the key issue is to spend the surplus that's been mounting up in a productive manner for the Society. He encourages strategic thinking and spending big chunks of money in a manner which will be recognized as great thinking and activity down the road. Further discussion followed and Choma recommended that a representative from DAC become a part of some sort of an ad-hoc committee in helping the Society reach various proposals and goals. Choma will take it upon himself (as part of PAC) to proceed with this strategic plan (perhaps with Dunlop's help). Choma summarized that there is obviously a need for a strategic plan to be developed with the help of various individuals. Reddy added that he would like to underline the urgency of this matter.

Sherif Michael, co-chair of ISCAS'98, presented a status report on the symposium. He indicated that the close proximity of ISCAS'98 to Silicon Valley can be used as a means to promote possible CAS membership. On behalf of the entire ISCAS'98 committee, Michael solicited input/suggestions from everybody in the months to come.

Technical Activities

Chris Toumazou, vice president for technical activities, presented a report of the Technical Activities Division, detailing the activities from all of the CAS technical committee chairs.

He also presented and submitted a proposal by the Technical Activities Division to offer Advanced Continuing Education (ACE) short courses. Toumazou emphasized that the concept of these ACE short courses would be "pulling together

the huge educational ingredient of the Circuits and Systems Society and exposing it within a very high quality international advanced continuing education program."

A launch date of September 1998 is being planned for the first of these (hopefully) many short courses. It has been proposed that the first course be offered in Thailand (Phuket Island) since Toumazou has already approached Mahanakorn University in Thailand and they have agreed to co-sponsor the event. To this end, Toumazou was now requesting that CAS become the other official co-sponsor of the short course in Thailand and also provide him with a budget.

When asked who will coordinate this project, Toumazou replied that the Continuing Education office at Imperial College, London, would initially. He hopes, however, the Society will eventually have an administrative office in order for all future short courses to be coordinated directly through it.

Toumazou stressed that these courses must be very industry-oriented and very specific for niche problems *now*. He feels that such courses should not be run in the United States because there is too much competition. Europe also offers a wide variety of these type of short courses but there is definitely a need in central Europe.

A motion was put forth to fund the Technical Activities Division ACE initiative in the amount of \$50K. It was seconded and passed unanimously.

Regional Activities Division

Rui de Figueiredo, president-elect and chair of regional activities, gave a brief description of the President's Advisory Council (PAC) which he chairs. He also indicated that PAC was formed in the context of President John Choma's vision statement at the beginning of his term. The areas covered by PAC are:

- Membership Development Initiative* - Bell Sheno
- Undergraduate Education/ Research* - Yannis Tsividis
- Graduate Education/Research* - Ken Jenkins
- (In addition, Chris Toumazou is helping out Tsividis and Jenkins with their initiatives.)
- Continuing Education* - John Choma
- International Activities* - Ruey-wen Liu
- Societal Infrastructure* - Michael Lightner

White papers will be prepared by PAC members in their respective areas and distributed to the Board. De Figueiredo welcomes input from everybody.

Bell Sheno, a member of PAC charged with membership development, presented some short-term and long-term plans to encourage membership growth.

Then, at the request of Sheno, as chair of the CAS Distinguished Lecturer Program, a motion to approve the 1997-1999 List of CAS Distinguished Lecturers was made and seconded. It passed with a vote of eleven (11) - yes, and one (1) - no. Four members abstained because their names appear on the list. Choma announced that names can always be added to the list.

SPRING 1997, HONG KONG, REPORT

Past President's Report

Michael Lightner, past president, reported that he had attended the last IEEE TAB meeting on behalf of John Choma. He indicated that there are major developments happening in Region 9 and a lot of enthusiasm to work with the Society.

Lightner also announced the winners of the 1997 CAS Awards. Congratulations to all the winners. Lightner has promised to work closely with Choma on getting many more nominations for these awards in the future. [*This year's award winners were announced in the June issue of the Newsletter; and an announcement and forms for next year's awards are on pages 4 and 5 in this issue.—editor*]

Finally, Lightner informed the Board that the IEEE has left it up to the societies to decide the benefits they want to extend to their life members. Thus, a motion was put forth that from this point hence, the CAS Society offer its life members one Transaction of their choosing each year. The motion was seconded and passed unanimously.

Administrative Report

On behalf of Cliff Lau, vice president for administration, Choma presented the Administrative Report.

Choma emphasized that some of the new initiatives being taken will require that the Society spend money, e.g., Chris Toumazou's ACE initiative and a possible administrative office. Choma then discussed the need of having a central location for administrative support. In conjunction with this issue, there is an immediate problem of payment for Barbara Wehner's salary. A contract to the University of Illinois at Chicago will be prepared (with overhead) effective July 1, 1997.

Choma pointed out that, in the long run, the Society will need an administrative office possibly staffed by 1–3 people who would handle, in addition to Barbara Wehner's current responsibilities, the entire process of publications/transactions. Whatever the central administrative office will do, there will be costs involved with it.

In summary, Choma indicated that all of the initiatives discussed will need to be funded; and this money must come out of the Society's budget/reserves. Choma suggests taking about 5% of the Society surplus and start paying for opportunity requirements.

Choma then presented the *1997 and proposed 1998 Budget Parameters*. He announced that this is the time to make any changes to the proposed 1998 budget. Once

all input is received, a revised proposed 1998 budget will be distributed to the entire Board.

A motion was put forth to approve the proposed budget subject to the proviso that final figures will be provided. The motion to approve was seconded, and passed with a vote of fourteen (14) - yes, one (1) - no, and one (1) - abstention. It was also noted that Choma and Lau need to work together on finalizing the budget as soon as possible.

At this point Choma announced that the meeting was already running late. With the permission of Franco Maloberti, Paulo Diniz, and Graham Hellestrand, Vice Presidents for Region 8, 9 and 10, respectively, Choma announced that he would like to conclude the meeting since the regional reports were all included in the *Agenda*. In addition, a *CAS Report from IEEE Region 8*, by Professor Anthony C. Davies, IEEE Region 8 Coordinator, was included as an enclosure.



1998 IEEE International Symposium on Circuits and Systems

May 31–June 3, 1998
Monterey, California

<http://www.iscas.nps.navy.mil>

LAST CALL FOR PAPERS



The 1998 IEEE International Symposium on Circuits and Systems will be held in Monterey, California at the Monterey Conference Center, May 31–June 3, 1998. The symposium is sponsored by the IEEE Circuits and Systems Society and hosted by the Naval Postgraduate School. The symposium will include regular sessions on customary and recent topics; special sessions on emerging circuits and systems topics; plenary sessions on selected advanced aspects of the theory, design, and applications of circuits and systems; and short courses given by experts in specific state-of-the-art subject areas. The symposium will be organized into parallel lecture sessions and poster sessions. Papers suitable for poster presentations are those that require interactive discussion; otherwise, poster and lecture presentations carry equal weight.

Prospective authors are invited to submit their papers reporting original work as well as tutorial overviews in all areas of circuits and systems.

Authors are invited to submit extended summaries (1000–2000) words) of their papers along with a cover sheet for review by the Technical Program Committee. Submissions can be sent to:

Professor Murali Tummala, Code EC/Tu
ISCAS98 Technical Program Co-Chair
Department of Electrical and Computer Engineering
Naval Postgraduate School
Monterey, CA 93943, USA

Authors' Schedule:

Deadline for Submission of Summaries:	October 1, 1997
Notification of Acceptance:	December 15, 1997
Deadline for Submission of Camera-Ready Paper:	January 30, 1998

Check the symposium web site for up-to-date information:

<http://www.iscas.nps.navy.mil>

Send any general questions to info@iscas.nps.navy.mil.

Conference Secretariat numbers: phone: 408–656–5073; fax: 408–656–5074

CONFERENCE REPORT

3rd ICECS Region-8 CAS Conference

The Third IEEE International Conference on Electronics, Circuits, and Systems (ICECS '96), a regional conference of the IEEE Circuits and Systems Society, was held on the beautiful island of Rodos, Greece, during October 13–16, 1996. The conference site was the Rodos Palace Hotel and Convention Center, sitting on a 30-acre site in the middle of the magnificent Ialysos Bay, overlooking the Aegean Sea.

The conference served as a forum, where many important advances in science and technology were presented and where industry and academia came together to present and discuss state-of-the-art technological developments. In a truly international meeting, delegates came from 43 countries from five continents to present their work.

This year, the conference was officially recognized as a regional conference of the Circuits and Systems Society, supported financially by Region 8 of IEEE, three CAS technical committees and many other local and international sponsors. This helped significantly in the organization efforts, which resulted in several breakthroughs:

1- More than 500 papers were submitted of which 340 were accepted, based on reviews received by three reviewers for each paper, and presented in 60 sessions organized in 4 parallel tracks.

2- Three excellent plenary speeches were given during the conference. Dr. Franco Maloberti of the Univ. of Pavia, Italy, talked about "Research and Innovation: A New Route to Development" and gave an answer to the key and controversial question: can high-technology (electronics/microelectronics) contribute to development? Dr. Dave Broster of the European Commission, DGIII Industry, talked about "Stimulating the Take-Up of Micro-electronic Solutions" by the industrial sector to help in developing competitive products for sale on global markets. He described the ways in which the ESPRIT programme has evolved to respond to these industrial needs. In a more technical speech, Dr. W. Kenneth Jenkins of the University of Illinois, talked about "Current Practices and Future Directions for Adaptive Signal Processing," with particular attention devoted to filter structures, adaptive algorithms, and practical implementations for various applications.

3- A total of four successful tutorials were offered during the first day of the conference: Low-Power Design, co-organized by Kaushik Roy, of Purdue University and Rabindra

(Rob) Roy, of NEC Research; Introduction to Computer Systems and Network Performance Evaluation, organized by Dr. Mohammad S. Obaidat, of Monmouth University; Recent Progress in Modeling and Simulation of High-Speed VLSI interconnects, organized by Dr. Michel Nakhla, of the Carleton University; and Integrated Circuit Broadbanding, organized by Dr. John Choma Jr., of the University of Southern California.

4- There were three themes, around which successful special sessions were organized: Opto-Electronic Computing Systems, Voice/Audio Processing Systems, and Testing of Analog and Mixed Signal Circuits.

5- Four sessions were dedicated to the presentation of the results of a selection of successful European Union ESPRIT projects.

6- About 100 papers were presented in posters.

7- ICECS '96 drew papers from 51 countries. After the review was completed, papers from 43 countries from all five continents were accepted.


8- A small technological exhibition was held, along with a book exhibition.

9- The proceedings of the conference will be available and distributed to libraries around the world through the IEEE book broker program.

The consensus among the SC meeting participants was that the success of the third ICECS characterized it as a turning point in the history of the conference. For the first time, there were many delegates from the far east. Proposals to host future ICECS meetings came from several countries from both within Region 8 and from outside the region including Italy, Finland, Norway, Cyprus, Saudi Arabia, and Taiwan. The 4th ICECS will be held in Cairo, Egypt in December 1997 and the 5th will be held in Lisbon, Portugal, in 1998, the site of the 98 World Expo, the last World Expo this century.

In addition to the technical program, the participants had a chance to partici-

pate in tours around the island of Rodos and neighboring islands. The welcome reception was held in the magnificent Palace of the Grand Masters in the Old Town of Rodos, which the participants had a chance to tour. It was accompanied by a performance of the City Folk Dance Group and a multimedia show on the rich history of the island that revealed its multicultural Mediterranean heritage. The Banquet dinner was



98
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ICECS
5th IEEE
International Conference on
Electronics, Circuits and Systems

September 7–10, 1998
Instituto Superior Técnico
Lisboa, Portugal
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Deadlines
Receipt of camera-ready manuscript: **January 30, 1998**
Notification of acceptance: **April 3, 1998**
Proposals for Tutorials and Special Sessions may be submitted to the Conference Secretariat on or before **December 16, 1997**

For further information, contact:
ICECS'98 Secretariat
Instituto Superior Técnico, Center of Microsystems
Av. Rovisco Pais, 1, 1096 Lisboa Cedex, Portugal
Tel.: +351 1 8417677 Fax: +351 1 8417675
E-mail: icecs98@ecsm4.ist.utl.pt

Web Site: <http://ecsm3.ist.utl.pt/icecs98>

COMMITTEE REPORT

VLSI Systems and Applications Technical Committee

Eby G. Friedman
University of Rochester
Dept. Electrical Engineering
Rochester, New York 14627

The following is a summary and review of the activities of the VLSI Systems and Applications Technical Committee (VSA-TC) during the 1995 and 1996 time period. Another goal, in addition to providing some information describing our technical committee's recent successes, is to pass the torch to our new chair and secretary. But first let us thank our earlier leadership, the two most recent being Prof. Steve Kang and Prof. Magdy Bayoumi. They, among others, have provided the focus and momentum that have permitted this TC to grow and develop as much as it has. I have no doubt that our new leadership, Prof. Bing Sheu as chair and Prof. Thanos Stouraitis as secretary, will lead our technical committee to ever greater levels of success.

The past two years of our technical committee have been fundamentally exciting and successful, in that we have added considerably to the health and well being of our society, Circuits and Systems. We have supported to a very great extent both the ISCAS last year in Atlanta and the recent ISCAS in Hong Kong. Specifically, our TC designed the VLSI track for both meetings, proposed and implemented special sessions and tutorials, participated fully in the review process, and generally supported the high standards and goals of this society's capstone conference.

Specific efforts developed for ISCAS'97 include two tutorials and one special session. The tutorials were "High Performance VLSI Signal and Video Processors" presented by Drs. Keshab Parhi, Bryan Ackland, and Peter Pirsch, and "High Speed VLSI Interconnects" by Dr. Michel Nakhla while the special session, organized by TC member, Dr. Naresh Shanbhag, was on "VLSI Systems Design for High-Bit Rate Communications." Prof. Keshab Parhi also organized a tutorial for ISCAS '96 on the "Design of High-Performance DSP

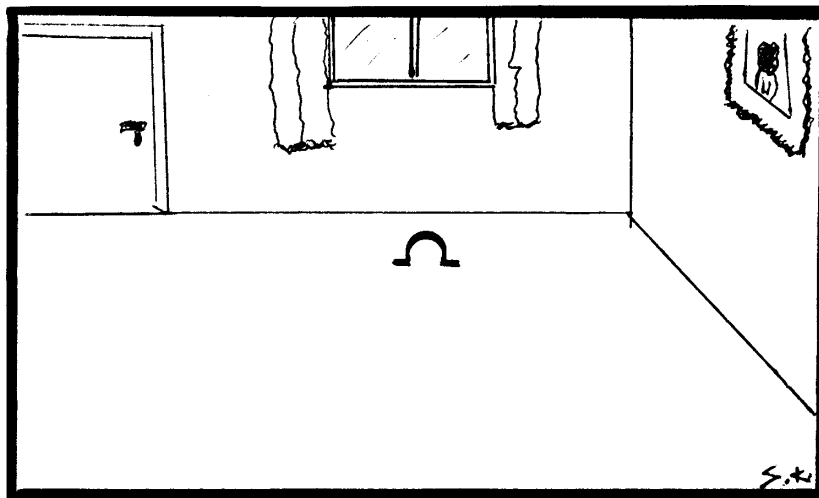
Systems," presented by himself and Prof. Francky Catthoor. These topics demonstrate the importance and currency of our existing TC membership.

Furthermore, our committee supported the 1996 Midwest Symposium with a special session proposed, arranged, and chaired by Prof. Mohsin Jamali on "VLSI Signal Processing for Satellite Communications." Our TC was also one of the three sponsoring CAS technical committees of the Third International Conference on Electronics, Circuits, and Systems (ICECS '96), chaired by our new secretary, Prof. Thanos Stouraitis. This meeting was a great success and this conference is an important element in the IEEE Circuits and Systems Society developmental activities and focus on the Mediterranean region. (For a detailed review, see the conference report by Professor Stouraitis on the facing page.)

Our TC is also a cosponsor of the 1997 IEEE Workshop on Signal Processing Systems (SiPS) (formerly named the IEEE Workshop on VLSI Signal Processing) with our sister technical committee from the Signal Processing Society, the Design and Implementation of Signal Processing Systems Technical Committee (DISPS-TC). This new relationship is an important step for our technical committee, one which has been in development for a number of years and has finally been realized (thanks go out to Prof. Graham Jullien, Prof. Magdy Bayoumi, Prof. Keshab Parhi,

and many others). Furthermore, this type of cosponsoring relationship between IEEE societies is the right kind of relationship for both our societies in that these relationships create fewer conferences of higher quality. Let me use this forum to recommend attending this workshop in Leicester, UK this November. Additional information describing this workshop is included in this magazine's description of upcoming confer-

THE ADVENTURES OF ...
...THE 'UMBLE OHM
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"OHM ALONE. . ." The original movie

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FALL 1997 CONFERENCE AND EXPOSITION VHDL: RAPID SYSTEMS PROTOTYPING WITH VHDL

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March 8-10, 1998

West Virginia University
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For Further Information Contact:

Dr. Ali Feliachi, SSST98 General Chairman
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Hilton Hawaiian Village, Hawaii
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This symposium is a challenging international forum for researchers and practitioners interested in nonlinear phenomena and their efficient applications.

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Prof. Mamoru Tanaka
EEE Dept., Sophia Univ.
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E-mail: nolta97@mamoru.ee.sophia.ac.jp

For up-to-date information:

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I ♦ C ♦ C ♦ A ♦ D ♦ 97

A CONFERENCE FOR THE EE CAD PROFESSIONAL

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The International Conference on Computer-Aided Design, ICCAD-97, is sponsored by the IEEE Circuits and Systems Society, the IEEE Computer Society and the ACM/Special Interest Group on Design Automation. It is held in cooperation with the IEEE Electron Devices Society.

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Delft Univ. of Technology
Lab. for Circuits & Sys., Elec. Eng. Dept.
Mekelweg 4
Delft, 2628 CD The Netherlands
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Conference Manager:

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ICM'97

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For Europe and North America, contact:

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1997 IEEE Workshop on SIGNAL PROCESSING SYSTEMS (SiPS) Design and Implementation

formerly IEEE Workshop on VLSI Signal Processing



IEEE

Design and Implementation of Signal Processing Systems

and now jointly co-sponsored for the first time by

IEEE Circuits and Systems Society Technical Committee on

VLSI Systems and Applications

SiPS
97

In cooperation with

Circuits and Systems Society Technical Committee on **Multimedia Systems and Applications**

Signal Processing Society Technical Committee on **Multimedia Signal Processing**

November 3-5, De Montfort University, Leicester, UK

The objective of this workshop is to provide a forum for discussion of new developments in the design and implementation of Signal Processing Systems including *VLSI and Systems Technology*, *DSP Technology*, and *Applications*. A hard-bound proceedings of the workshop will be published.

Workshop Theme: "Multimedia Systems on a Chip"

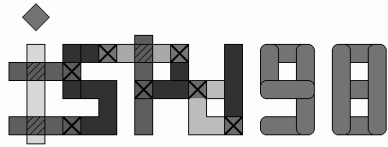
For more information, please visit our web site at <http://www.eng.dmu.ac.uk/~mki/sips97/> or contact the general chair **Mohammad Ibrahim** (email: mki@dmu.ac.uk).



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The International Symposium on Physical Design provides a forum to exchange ideas and promote research on critical areas related to the physical design of VLSI systems.

Important Dates:

Submission deadline: **December 5, 1997**
Acceptance notification: **January 26, 1998**
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Submission of Papers:

Authors should submit full-length, original, unpublished papers (maximum 20 pages double spaced) along with an abstract of at most 200 words and contact author information (name, street/mailling address, telephone/fax, e-mail). Please e-mail a single postscript file, formatted for 8 1/2" x 11" paper, compressed with Unix "compress" or "gzip" to ispd98@cs.utexas.edu.

Alternatively, you may send ten (10) hardcopies of the paper to:

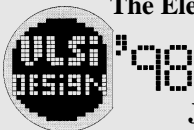
Prof. D. F. Wong
Technical Program Chair, ISPD-98
University of Texas at Austin
Department of Computer Sciences
Austin, TX 78712, USA

Symposium Information:

To obtain information regarding the Symposium or to be added to the Symposium mailing list, please send e-mail to ispd98@ee.iastate.edu. The ISPD web page is at:

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CALL FOR PARTICIPATION



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January 4-7, 1998
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For more information, contact the Publicity Chairs

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CALL FOR PAPERS

1998 IEEE Asia-Pacific Conference on Circuits and Systems

November 24-27, 1998
Chiangmai, Thailand

The 1998 IEEE Asia-Pacific Conference on Circuits and Systems (APCCAS'98) is the fourth in the series of biennial Asia-Pacific Conference sponsored by the IEEE Circuits and Systems Society and the National Electronic and Computer Technology Center of Thailand. It will be held at the Chiangmai Plaza Hotel, Chiangmai, Thailand, on 24-27 November 1998. The conference will be devoted to all aspects on theory, design, modeling, simulation, and applications of circuits and systems. Plenary sessions, special sessions, invited talks, and tutorials on specific advanced topics will also be included in the program. Lecture sessions and poster sessions will be treated equally in terms of review process. "Microelectronics and Integration System" is the theme of the conference.

AUTHOR'S SCHEDULE:

Extended summaries: April 15, 1998
Notification of acceptance: July 15, 1998
Camera-ready papers: September 15, 1998

SUBMISSION ADDRESS:

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Email: fuj@ss.titech.ac.jp

Conference Secretariat:

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Tel. : +(66-53)-94-4136.
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Please submit 4 copies of the paper with a cover page by October 10, 1997 to the symposium GENERAL CHAIR:

MAGDY A. BAYOUMI

The Center for Advanced Computer Studies
University of SW Louisiana, P.O. Box 44330, 2 Rex Street
Lafayette, LA. 70504
Phone: (318) 482-6853; Fax: (318) 482-5791
e-mail: mab@cacs.usl.edu

Important Dates:

Paper Submission: **October 10, 1997**
Notification of Acceptance: **November 20, 1997**
Camera-Ready Paper: **December 15, 1997**

Information:

<http://www.cacs.usl.edu/~vlsi98>

Vincenzo Piuri, Politecnico di Milano, Department of Electronics and Information piazza L. da Vinci 32, 20133 Milano, Italy
phone: +39-2-2399-3623 fax: +39-2-2399-3411
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October 6-8, 1997

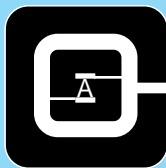
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IEEE **CIRCUITS AND SYSTEMS** **NEWSLETTER SOCIETY**

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held in the superdome that covers the pools at the Rodos Palace Hotel and was also accompanied by a music and dance group that made most participants dance to the tunes of popular Greek songs.

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ences. Special thanks should also be given to Prof. Mohammad Ibrahim, who is the program chair of this workshop and a member of both our TC and the DISPTC, to Prof. Jan Rabaey from the DISPTC, both of whom greatly supported this cosponsoring relationship, and to Prof. Peter Pirsch, who co-chairs the technical program and is a member of our TC.

Members of our TC also participate fully on a number of editorial boards and conference program committees within the Circuits and Systems Society and we are proud to announce that our current chair, Prof. Bing Sheu, is now the current editor-in-chief of the *IEEE Transactions on VLSI Systems*. A special issue on Low Power Wireless Communications was very recently published in the society transactions, *IEEE Transactions on Circuits and Systems II: Analog and Digital Signal Processing*, co-edited by Profs. Sayfe Kiaei and Eby Friedman. Significant effort by many members of our TC were made in the editorial and review activities required by these and many others journals and conferences, activities fundamental to the health of our society and technical committee.

Let me end by saying that I have greatly enjoyed my participation within this technical committee, in particular working with a number of highly intelligent, energetic, and stimulating people. I look forward to continuing these interactions. I would also like to wish both Bing and Thanos the very best of success and good fortune during the next few years and recommend to all the members of our technical committee that we actively support their endeavors and activities.

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