

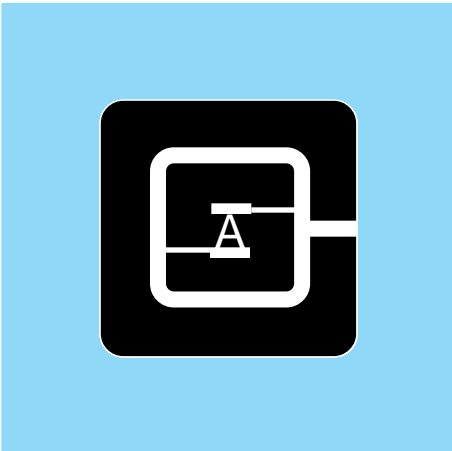
IEEE

CIRCUITS AND SYSTEMS

NEWSLETTER SOCIETY

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In This Issue...

The Vision of Our New President—Discover the Possibilities, page 3
CAS Constitution—Minor Changes You May Need to Know, page 12
It's Nominations Time Again for CAS BOG and Officers, page 16

President's Message	3
CAS Officer Election Returns	4
The Adventures of the 'Umble Ohm	5
CAS 1996 IEEE Fellow Profiles	6
Board of Governors' Report	10
Constitution and Bylaws Changes	12
Calls for Papers	
BCTM '97	14
SiPS '97	14
ICM '97	14
NOLTA '97	15
NICROSP '97	15
Coming Conferences	
ISCAS '97	13
DAC '97	13
MWSCAS '97	15
ECCTD '97	14
MIXDES '97	14
IMST '97	14
ICNN '97	14
NDES '97	15
NSIP '97	15
ICEC '97	15
ISPD '97	15
BOG/Officers Nominations Announcement	16

DECEMBER

SEPTEMBER

JUNE



IEEE Circuits and Systems Society Newsletter

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A VISION OF THE FUTURE

Message from the New President

John Choma, Jr.
Electrical Engineering
University of Southern California
Los Angeles, CA 90089-0271

It is a singular honor and great privilege to be afforded the opportunity to share my vision, goals, and ideas with the membership of the IEEE Circuits and Systems Society. More than simply expounding my views, I should hope that this article motivates interested members to provide me or any of the other officers of our Society with constructive critique, commentary, and suggestions that can help reaffirm and enhance our long standing tradition of unimpeachable technical excellence and enviable technical vibrancy. My invitation for periodic feedback is not an idle offer, for fundamentally, my personal hope is to be remembered as a president who responded positively to the meaningful wishes and requirements of the membership in both the academic and the industrial sectors of our technical community.

The office of president is a humbling honor that exceeds even the most liberal of my personal career objectives. A scant decade or so ago, I would not have viewed myself as capable of competently assuming the responsibilities implicit to the CAS presidency. But I am proud to say that I assume this presidency with a supreme confidence and comfort that derives from years of a close working relationship with three paradigms of leadership excellence. In particular, I served Michael Lightner last year as the president elect, I worked with Ruyewen Liu and Wai-Kai Chen as vice president for administration, and I also worked with Wai-Kai Chen for several years in a variety of projects, including associate editor of the *IEEE Transactions On Circuits and Systems*. My level of confidence is further bolstered by an awareness that I can continually solicit constructive critique, advice, and direction from an Executive Committee whose leadership stature in the electrical engineering profession is both well-earned and beyond reproach. Specifically, Rui J. P. de Figueiredo is our president elect, Chris Toumazou serves as vice president for technical activities, Hari C. Reddy is our vice president for conferences, Yih-Fang Huang is our vice president for publications, Franco Maloberti “rules” as vice president for Region 8, Paulo S. R. Diniz “governs” as vice president for Region 9, Graham Hellestrand joins us as vice president for Region 10, and, of course, Michael Lightner remains on the Executive Committee as past president.

Power in the IEEE Circuits and Systems Society is vested


in the technical, conference, and publication divisions, as well as in an advisory panel to the president, which Rui de Figueiredo has agreed to form and chair. The following paragraphs effectively overview my societal vision by providing information on the charges I have asked these three divisions and single panel to assume.

Advisory Panel

In the interests of achieving a year to year continuum in all aspects of societal operations, this panel is charged with the responsibility of forging, and ultimately formally proposing to the Board of Governors, a three year strategic plan for our Circuits and Systems Society. It will focus on five outreach issues that are intimately related to our fundamental mission of ensuring that the Circuits and Systems Society continues to be maximally responsive to the needs of its members. These issues are (1) international outreach, (2) national outreach in the academic sector, (3) national outreach in the industrial sector, (4) outreach to state and Federal governments, and (5) outreach to potentially new society members, inclusive of students. The panel will actively solicit suggestions from, and engage the cooperative efforts of, all society officers, the members of our Board of Governors, and the interested membership.

As a university educator for approximately twenty-five years and as an educator who continues to teach introductory level courses on circuit and system analyses, I am concerned about the present state of circuits and systems education in at least the United States. Having visited dozens of universities and talked to many more than dozens of my educational peers, I detect an ominous and ostensibly widespread view that the first circuits course is so simple and so straightforward that anyone on the faculty can teach circuits to engineering neo-

phytes. This statement is akin to my teaching a course on antennas. In particular, I am sure that I can follow an acceptable textbook on antennas and could probably even present this material understandably. But necessarily lacking from my presentation would be the insights that serve to inspire students to reach beyond textual material. When I teach the first circuits course, I spend almost zero time reading and studying the textbook material associated with a series of lectures. But I expend considerable time and energy developing examples,



John Choma

Office open

“My invitation for periodic feedback is not an idle offer”

CAS Officers Election Results

As you know, a ballot for the election of officers to the Board of Governors of the IEEE Circuits and Systems Society was issued on October 11, 1996. The votes have been counted, and the following candidates have been elected to serve for a one-year term beginning January 1, 1997.

President-Elect

Rui J. P. de Figueiredo

Vice President-Conferences

Hari C. Reddy

Vice President-Publications

Yih-Fang Huang

Vice President-Technical Activities

Chris Toumazou

Vice President-Region 8

Franco Maloberti

Vice President-Region 9

Paulo S. R. Diniz

Vice President-Region 10

Graham R. Hellestrand

Administrative Vice President

Clifford G. Lau

There was a tie vote for the position of administrative vice president, so the election of this candidate is a result of the December 3, 1996, run-off ballot.

We wish the newly elected officers success and thank all candidates for their willingness to serve and for permitting their names to be included on the ballot.

—Robert T. Wangemann
Managing Director
IEEE Technical Activities

many of which derive from my personal research and consulting experiences, that delineate the practicality, utility, propriety, and indeed the limitations of the theoretic material undergoing study.

In view of the foregoing commentary, I ask the advisory panel to encourage the leadership of our Circuits and Systems Society to assume a major role in the definition of, and suggested implementation strategies for, optimal undergraduate and graduate circuits and systems curricula. In my view, basic circuits and systems education has not kept pace with rapidly emerging electronics and related technologies because no knowledgeable and respected organization, like our society, has taken the initiative to define the prerequisites for a satisfying circuits and systems education. As a result, publishers of cognizant texts are not well calibrated because for the most part, they rely strongly on the existing sample space of faculty who teach first circuits courses. But many of these faculty are less than completely immersed in circuits and systems technologies.

The advisory panel is accordingly asked to explore defining suitable curricular matter pertinent to introductory circuits and systems education. A broad topic warranting extensive discussion is the problem of preparing bright young students for the modern electronics/computers/communications age, without compromising an insightfully sound awareness of fundamental theoretic concepts. The questions that the advisory panel might ask and ultimately answer in their relevant deliberations include the following. Where should students first gain an awareness and appreciation of digital signal processing and the design role played by digital filtering? How can circuits simulations using SPICE be designed to maximize analytical and ultimately, design-oriented understanding? Where does behavioral analysis, particularly in regard to mixed signal integrated circuits, fit into the undergraduate electrical engineering program? Where do hardware description languages fit?

To be sure, the foregoing charge is uncharted territory for our society, for in the past, we have influenced curricular development and implementation in a largely *de facto* manner. In this case, I am asking the advisory panel and indeed everyone on the

Executive Committee and the Board of Governors to ponder the possibility of our proactive involvement in the establishment of a norm for first rate circuits and systems education.

At the discretion of the Board of Governors, I plan to share our educational disclosures with university officials worldwide, publishers, and cognizant personnel at the National Science Foundation. I would also ask that all members of the Executive Committee and the Board of Governors share our findings with their interested contacts.

Technical Division

The Circuits and Systems Society is generally viewed as an academic community motivated almost exclusively by theoretical research appreciated only by the academic community. To this end, we need make no apologies and indeed can boast countless basic research disclosures that have ultimately benefited the engineering community and society at large. But seemingly lost in the foregoing perception is that a large percentage of our membership, and indeed our leadership, are extremely competent engineers and extraordinary educators who are ever mindful of the pragmatic implications of the research programs in which they are immersed. I think it unfortunate that the majority of the circuits and systems engineering community is ostensibly unaware of these attributes and is rarely given the opportunity to hear our technical leadership and witness first hand their remarkable skills and laudable creativity.

To the foregoing end, the Technical Division is asked to explore the formal establishment of short course/workshop programs aimed largely for the industrial community. I also charge the Technical Division with the task of establishing the appropriate infrastructure for the ongoing conduct of short course/workshop programs. Such an infrastructure may entail the establishment of a new Executive Committee office in the form of a vice president for education. At a minimum, I imagine a substantial broadening of the scope of our extant Distinguished Lecturer Program, which has been so capably managed by Bell Sheno (who has also agreed to serve as a special advisor to me). I also envisage a short course catalog, coupled with the identification of suitable expert lecturers for each identified short course. The catalog and identified lecturers would be suf-

ficiently fluid to reflect current industrial interests, societal focus and extant expertise, emerging new technologies, and the flux of competent technical personalities who continually surface in a society such as ours. The CAS Society would effectively act as an agent for these lecturers by arranging course presentation times and sites and negotiating relevant fees. Aside from establishing a new revenue stream for our society, the fees would hopefully provide generous lectureship honoraria. Most importantly, the short course program would complement our critical mission of servicing both our membership and the entire electrical engineering community on a global scale.

Conference Division

The continued technical vitality of our society depends on our ability to engage other IEEE societies and councils in mutually beneficial, cooperative endeavors. We already have such endeavors in place with Design Automation and such other organizations as the Neural Networks Council, the Computer Society, and the Solid State Circuits Society. I think it appropriate that interaction be attempted with the Electron Devices Society, the Hardware Description Language (HDL) group, the Communications Society, and the Power Society.

I ask that the Conference Division investigate how the CAS Society might invigorate our extant interactive programs and explore plausible new cooperative programs with societies, such as those mentioned above. Three goals are implicit to this task. The first is to explore beneficial new ways of utilizing DAC surplus funds to support appropriate workshop, short course, conference, and other activities in our various international regions. The second is

to formulate and implement mechanisms to ensure that the CAS Society is kept continually abreast of emerging technologies that impact the circuits and systems community. This information is pivotal to proposing special conference sessions that encourage enhanced attendance and special *Transactions* issues that conduce increased readership. The third is to contribute CAS expertise and leadership to an extended technical community through our proactive involvement with conferences, meetings, and workshops that complement our technological mission.

Publications Division

Regardless of the motivations underlying the naming of our present two CAS *Transactions*, an apparently widespread perception among our membership is that very little difference in subject emphasis separates our *Part I* and *Part II Transactions*. Accordingly, the Publications

Letters, as articulated over the past year by Rui de Figueiredo.

I also ask that the Publications Division study two omnipresent issues. The first is the apparent widespread confusion that prevails over the purpose, nature, format, and even propriety of our *Circuits and Systems Exposition (CASE)*. Whatever decision is ultimately reached, we must ensure that the overall intent and scope of *CASE* is very clearly articulated in our *Transactions* and perhaps in our *Newsletter*. For example, is *CASE* to focus exclusively on tutorial issues? Is *CASE* to comprise well written technical notes that might be used in senior level courses or in short courses offered to interested parties outside formal academe? In short, what is *CASE*, whom is it to address, what is its scope, how should it be written, etc.?

The other critically important issue is manuscript review time and the corresponding time required for actual publication. I ask that the Publications Division ponder revised editorial strategies that significantly reduce publication cycle time without compromising technical and presentation quality. To this end, it must solicit the active participation and cooperation of all our editors.

New Programs, New Members

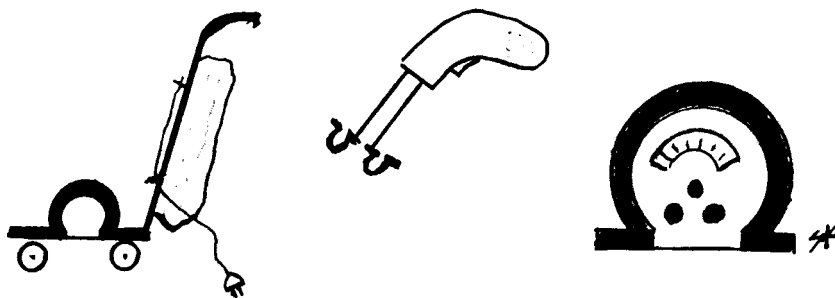
As president, I intend to work closely with the president elect and the regional vice presidents to ensure

that meaningful, efficient, and innovative procedures are in place to effect the continuing expansion of our various regional activities. I want our society to act properly and promptly to what the regional officers want to do and what they need to accomplish their laudable goals.

Finally, we need to be mindful of attracting new members to our society.

THE ADVENTURES OFTHE 'UMBLE OHM

...Shlomo Karni



Ohm appliances: vacuum cleaner,
hand mixer, radio

Division is asked to investigate the propriety of renaming our two CAS *Transactions* and defining, in as clarion a fashion as possible, the specific intent, goals, and nature of acceptable content pertinent to each of the resultantly renamed journals. Any restructuring that may result from committee deliberations must account for the recently established CAS

IEEE CAS FELLOW PROFILES 1997

Guanrong Chen

For fundamental contributions to the theory and applications of chaos control and bifurcation analysis.



Guanrong
Chen

After the “Chinese cultural revolution” (1966-77), Guanrong (Ron) Chen received the M.Sc. degree in computer science from Sun Yatsen (Zhongshan) University in 1981, and the Ph.D. degree in applied mathematics from Texas A&M University in 1987. In 1990, after visiting Rice University for three years, he joined the faculty of the Department of Electrical and Computer Engineering, University of Houston, Texas, where he is currently a tenured associate professor. He received the 1993 Junior Faculty Research Award from the college and, in 1994, was a summer visiting professor to the Nacional Universidad del Sur of Argentina, and a research consultant for the McDonnell Douglas Aerospace Systems and the Dow Chemical Companies. His main research interest is within the area of nonlinear systems, in both dynamics and control.

In addition to his 70 some journal papers and numerous conference abstracts, Dr. Chen has coauthored several research monographs and textbooks including *Signal Processing and Systems Theory: Selected Topics* (Springer, 1992), *Nonlinear Feedback Control Systems: An Operator Theory Approach* (Academic Press, 1993), *Linear Stochastic Control Systems* (CRC Press, 1995), *Hopf Bifurcation Analysis: A Frequency Domain Approach* (World Scientific, 1996), and *From Chaos to Order: Perspectives, Methodologies, and Applications* (World Scientific, 1997).

Dr. Chen has served as associate editor for the *IEEE Transactions on Circuits and Systems—I* from 1993–95, and for the *Chinese Journal of Control Theory and Applications* since 1995. He also serves as referee for more than twenty IEEE Transactions and systems engineering journals.

Patricia D. Daniels

For contributions to engineering education.



Patricia D.
Daniels

Patricia D. Daniels received the B.S. and Ph.D. in electrical engineering and computer science at the University of California at Berkeley. She has worked for the Aerospace Corporation, Westinghouse Aerospace, and the Boeing Company in the areas of system simulation, optimal control and estimation. She was on the faculty of the Department of Electrical Engineering at the University of Washington from 1974–1986. She then joined Seattle University where she served as professor and chairperson of the Department of Electrical Engineering until 1994. She is currently associate dean of the School of Science and Engineering and director of the Science and Engineering Project Center. In 1991–92, Dr. Daniels served as a program director in the Division of Undergraduate Education at the National Science Founda-

tion. She was chair of the electrical engineering division of the American Society for Engineering Education in 1992–93 and she served on the IEEE Education Society administrative committee from 1989–1994. She was local arrangements chair for ISCAS’95 and is currently on the Board of Governors of the IEEE Circuits and Systems Society. Dr. Daniels is a member of the Engineering Accreditation Commission of ABET and has served on the Board of Directors of the Women in Engineering Program Advocates Network. She is a member of Phi Beta Kappa and is a registered professional engineer in the state of California. In 1991 she was named Academic Engineer of the Year by the Puget Sound Engineering Council and in 1994 she received the IEEE Education Society Meritorious Service Award.

CIRCUITS AND SYSTEMS SOCIETY MEMBERS

H. Clark Bell

For advancements in synthesis techniques and development of new prototype networks for microwave filters.

H. Clark Bell received the B.S. degree in physics, and the M.S. and Ph.D. degrees in engineering from the University of California, Los Angeles, in 1966, 1969, and 1974, respectively.

From 1966 to 1968 he was with Rantec Corporation, Calabasas, California, working on ferrite phase shifters for phased array antennas and other microwave components. From 1969 to 1975 he was with Hughes Aircraft Company, El Segundo, California, working on ferrite phase shifters, millimeter-wave solid-state components, and multiplexers for satellite transponders. Since 1973 he has been responsible for a wide variety of RF filters and passive components for space, airborne and ground systems, with emphasis on high power, extreme environments, and system integration and interference suppression. From 1975 to 1977 he was with Wavecom, Northridge, California. From 1977 to 1981 he was with Microwave Applications Group,

Chatsworth, California. He returned to Loral Microwave-Wavecom in 1981, where he became vice president, engineering, followed by a temporary assignment in 1992 to Loral Microwave-Narda West, Rancho Cordova, California. He is now a consulting engineer doing business as HF Plus from offices in Chatsworth and La Jolla, California, providing HF through microwave engineering services.

Since 1980, Dr. Bell has been a member of Technical Committee MTT-8 on Filters and Passive Components, and is currently a co-chair. He was a guest co-editor of the special issue on microwave filters, *IEEE Transactions on Microwave Theory and Techniques*, July 1994. He is a member of the recently-formed IEEE Standards Coordinating Committee SCC-34, Product Performance Relative to the Safe Use of Electromagnetic Energy.

Dr. Bell is a registered professional engineer in electrical engineering in California.



H. Clark
Bell

Vijay Vittal

For contributions to the development of the transient energy function method and its application to power system dynamic security assessment, and for leadership in power engineering education and research.

Vijay Vittal received the B.E. degree in electrical engineering from the B.M.S. College of Engineering, Bangalore, India, in 1977, the M.Tech. degree in electrical engineering from the Indian Institute of Technology, Kanpur, India, in 1979, and the Ph.D. degree in electrical engineering from Iowa State University, Ames, in 1982.

He joined the faculty of the Department of Electrical Engineering at Iowa State University in 1982 and currently holds the rank of professor. His research interests are in the area of power system dynamics, dynamic security assessment of power systems, power system operation and control, and application

of robust control techniques to power systems. He is the author and coauthor of several papers in his field. He has also co-authored a textbook entitled *Power System Transient Stability Assessment Using the Transient Energy Function Method*.

During 1993–1994, he was the program director of the Power Systems Program at the U.S. National Science Foundation. He is a recipient of the 1985 Presidential Young Investigator Award. In 1988 he received the NCR Faculty Award of Excellence. He also received the 1989 Iowa State University College of Engineering “Young Engineering Faculty Research Award.”



Vijay
Vittal

IEEE CAS FELLOW PROFILES 1997

Henrique S. Malvar

For contributions to the theory and practice of lapped transforms, fast multirate filterbanks, and signal coding.



Henrique S.
Malvar

Henrique S. Malvar received the B.S. degree from Universidade de Brasilia, Brazil, in 1977, the M.S. degree from Universidade Federal do Rio de Janeiro, Brazil, in 1979, and the Ph.D. from the Massachusetts Institute of Technology in 1986, all in electrical engineering. His main areas of interest include filter design, fast algorithms for transforms and filterbanks, and signal compression, coding, and enhancement. He has several publications in those areas, including the book *Signal Processing with Lapped Transforms* (Artech House, 1992).

Dr. Malvar is vice president of research and advanced development for PictureTel Corporation of Andover, Massachusetts. PictureTel is the worldwide leader in dialed videoconferencing products. Before rejoining PictureTel in 1993, Dr. Malvar was with the faculty of the Universidade de Brasilia

(UnB), Brazil, since 1979. At UnB he held positions as head of the Digital Signal Processing Group, which he founded in 1987, and associate head of the EE department. He also founded and ran the IEEE Signal Processing and Computer Chapter in Brasilia from 1990 to 1993. From 1984 to 1987 he was a consultant for PictureTel.

Dr. Malvar was the recipient of the Young Scientist Award from the Marconi International Fellowship in 1981, and the Senior Paper Award in Image Processing from the IEEE Signal Processing Society in 1992. He has served as technical reviewer for many publications of the IEEE Circuits and Systems, Computer, Communications, and Signal Processing societies, and has been a member of the Digital Signal Processing Technical Committee of the IEEE Signal Processing Society since 1992.

Gabriel Rebeiz

For the development of novel microwave and millimeter-wave antennas, receivers and circuits using micromachining techniques.



Gabriel
Rebeiz

Prof. Gabriel Rebeiz received his Ph.D. in electrical engineering from the California Institute of Technology in June 1988. He joined the faculty of the University of Michigan in September 1988, and was promoted to associate professor with tenure in September 1992. Professor Rebeiz has eight years of experience in planar antennas and phased arrays, receivers, integrated circuits and associated electronics at microwave and millimeter-wave frequencies. He pioneered silicon micromachining techniques for the development of compact, low-loss, high-performance microwave and millimeter-wave antennas, filters, couplers and low-noise amplifiers and oscillators. His group also demonstrated the first integrated millimeter-wave sub-system to date—a 94 GHz 4-channel

monopulse tracking receiver with IF beam control. He is currently involved in three-dimensional silicon micromachined components and packaging techniques, and in developing novel fully polarimetric 94 GHz integrated monopulse receivers and phased-arrays, and a 77 GHz imaging array for collision avoidance systems for automotive applications. Professor Rebeiz received the National Science Foundation Presidential Young Investigator Award in April 1991 at the age of 26 years old. He also received the URSI International Isaac Koga Gold Medal Award for Outstanding Research in August 1993. Professor Rebeiz has graduated ten Ph.D. students and two M.S. students. He is the author of 65 papers in international journals and has more than 120 conference presentations.

CIRCUITS AND SYSTEMS SOCIETY MEMBERS

Yinghua Min

For technical leadership in electronic testing and fault-tolerant computing.

Yinghua Min received the B.S. in mathematics, from Jilin University, Changchun, Jilin, China, in 1962, and the Ph.D. in electrical engineering from China Academy of Railway Sciences, Beijing, in 1966. (All were equivalent qualifications, because there were no degrees awarded in P.R.China during 1949-1980.) He is currently professor at the Institute of Computing Technology, Chinese Academy of Sciences, and director of the Center for Fault-Tolerant Computing, which he established in 1987 when he moved to the Institute.

He was the first to propose an automatic control system scheme using digital computers for railway classification yard in P.R.China when he worked with the China Academy of Railway Sciences during 1962-1987. He led a group to implement a speed control system using a digital computer at the Fang-Xi station successfully in 1975, and designed the first detailed diagram for Nan-Xiang station computer control system for which he was awarded the second level prize of national

technological developments in 1984.

Professor Min has published a textbook, *Logic Circuit Testing*, and more than 75 technical papers in international journals, international conferences and top Chinese journals.

He has been also very active in social activities. In 1985, he organized the First National Symposium on Fault-Tolerant Computing in P.R.China, and gave great impetus to the establishment of the technical committee on fault-tolerant computing under China Computer Federation, of which he is now chair. He organized the 1989 Joint Symposium on Fault-Tolerant Computing, the IEEE Second Asian Test Symposium in 1993, and the international workshop on computer-aided design, test and evaluation for dependability in 1996. He serves on the editorial board of *Journal of Electronic Testing: Theory and Applications* (JETTA), USA and three other journals in China. He has served on many program committees of IEEE international conferences.



Yinghua
Min

T. R. Viswanathan

For contributions to the design and implementation of mixed-signal integrated circuits and systems.

T.R.Viswanathan received the B.Sc. in physics from the University of Madras, India, the D.I.I.Sc. in electrical communications engineering from the Indian Institute of Science, Bangalore, and the M.Sc. and Ph.D., both in electrical engineering, from the University of Saskatchewan, Canada, in 1956, 1959, 1960, and 1964 respectively.

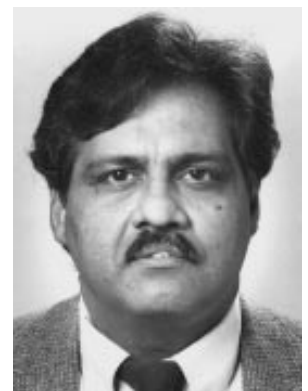
He returned to India in 1964 and helped build the Indian Institute of Technology at Kanpur where he was professor of electrical engineering, head of the Computer Center and dean of research and development. After returning to the U.S. in 1978, he served as professor of electrical engineering at the Uni-

versity of Michigan, Carnegie-Mellon University, and the University of Waterloo.

From 1985 to 1995, he was supervisor of analog integrated circuit design at AT&T Bell Laboratories where his group designed and introduced to manufacture many DSP-based mixed-signal products.

He is presently the director of the Circuit Technology Laboratory at the DSPR&D Center of Texas Instruments, Inc., where the research activities are in the design and testing of DSP's and mixed-signal integrated systems in advanced CMOS technologies.

Dr. Viswanathan was an associate editor of the *IEEE Transactions on Circuits and Systems*.



T. R.
Viswanathan

BOG Meeting Attendees

Society Officers:

Michael Lightner, *President*
John Choma, *President-Elect*
Hari Reddy, *VP-Conferences*

Rui de Figueiredo,
VP-Publications

Isao Shirakawa, *VP-Region 10*
Clifford Lau, *VP-Administration*
Ruey-wen Liu, *Past President*

BOG Members:

Magdy Bayoumi
Giovanni De Micheli
Geert De Veirman
Alfred E. Dunlop
Jose da Franca
Graham Hellestrand
Ken Martin
Bing Sheu
Mani Soma
Mona Zaghoul

Others:

Ibrahim Hajj, *BOG ('97-'99)*
Eby Friedman, *VLSI S&A
Technical Activities Chair*
Steve Kang
Fadi Kurdahi, *CAS Express
Letters Committee*
Barbara Wehner, *CAS Adminis-
trative Assistant*

Division Members

Conference Division:

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Giovanni De Micheli
Geert De Veirman
Alfred Dunlop
Dieter Mlynski
Mani Soma
Mike Lightner, *ex-officio*

Regional Activities Division:

John Choma, *Chair*
Paulo Diniz
Graham Hellestrand
Franco Maloberti
Kenneth Martin
Isao Shirakawa

Publications Division:

Rui de Figueiredo, *Chair*
Andreas Antoniou
Alfred Fettweis
Jose Franca
Robert Marks
Bing Sheu

Technical Activities Division:

Chris Toumazou, *Chair*
Magdy Bayoumi
George Moschytz
Mona Zaghoul

BOARD OF GOVERNORS' CORNER

The second semi-annual Board of Governors Meeting of the year for the Circuits and Systems Society took place at the Red Lion Hotel in San Jose, California, on Sunday, November 10, 1996, during ICCAD. After the preliminary roll call and welcome, a revised version of the agenda was approved, as were the minutes from the May meeting. An introduction of the members and their respective divisions took place as outlined in the side panel on the left.

Beginning the executive summary reports was the president, Michael Lightner. He noted that the Executive Committee, in conjunction with the wishes of the different divisions of the Board of Governors, has been working to develop a new budget format by which the society prepares and presents its yearly budget. Summarizing, the philosophy behind this is to: decentralize operations and decision making, empower each division, give each division a two-part budget—an operational budget for standard activities and an opportunity budget to seed new initiatives, and allow each division to manage its activities and budget while reporting to the ExCom regularly and presenting detailed budget requests and activity reports to the BOG.

Reporting on the progress of electronic publications, Lightner noted that the initiative is on schedule with the publications on the WWW in January, accessible only to registered member subscribers with passwords, etc. They will be put up six months at a time, though negotiations are still in progress with the Computer Society for the *T-VLSI*. The CD-ROM is on track, being set up to bring in revenue. This experiment needs to be monitored carefully to judge the affect on subscriptions and membership.

Lightner also summarized new and exciting developments of the latest Technical Activities Board (TAB) meetings. Items include: new bylaws for TAB give it autonomy and also allow more autonomy for CAS, Michael Lightner was elected to the TAB Management Committee, it is important to maintain strong society presence in TAB activities, Joe Bordogna is the new IEEE President-Elect, IEL is becoming more popular and may be sold to third party information providers, and the *Circuits & Devices Magazine* is marked for death at the end of 1997. Discussion on the *C&D Magazine* took place

with the conclusion that there is a "niche" this magazine fills which is not filled by other journals. In fact, all comments were positive. Lightner will report this to Mike Adler.

Mike Lightner also reported a number of issues from TAB that the CAS Society must address in the very near future. These are listed briefly in the side panel on page 11.

The next order of business was the proposal of five amendments to the Constitution and Bylaws, presented by Clifford Lau, administrative vice president. The first two proposed amendments were to clarify the membership of the Board of Governors since there is some confusion as to whether officers are members of the BOG. The first proposed amendment, to Article IV Section 1 of the Constitution, was to change the definition for the Board of Governors to include members of the executive committee; while the second proposed amendment, to Article V Section 1 of the Bylaws, sought to include the regional vice presidents as members of the executive committee. Both amendments passed with no opposition.

The third proposed amendment was made in order to streamline the society awards process. It was proposed that instead of having an award committee for each award, there be a single award committee chaired by the past president. This committee will do its work as subcommittees, but with a strict coordination by the past president. In essence, this simply makes the present award committees subcommittees. The motion to change the Bylaws, Article VI, Section 1, passed without opposition. The changes as made in these three amendments are shown on page 12.

The final two proposed amendments sought to address the confusion as to who should approve the many conferences and workshops that the CAS Society sponsors, co-sponsors, and cooperates on. These amendments were tabled for further discussion at the next BOG meeting.

As the next order of business, brief presentations were made regarding the divisional budget requests. Michael Lightner proposed that the president's budget be increased to \$20,000 in 1997 to support international travel in support of our members around the world, allow for other opportunity costs, and due to inflation. For the Conference Division,

FALL 1996 REPORT

Barbara Wehner, Admin. Asst.
Clifford Lau, Admin. V. P.

Hari Reddy requested a budget for conference related activities. In particular, he requested \$20K to be used for promotion purposes and \$30K for sponsorship of workshops. Ruey-wen Liu proposed an annual operating budget of \$5,000 for the office of the past president. Currently, the budget for the services of the past president is located in the miscellaneous budget category.

After the specific budget requests, Clifford Lau, vice president for administration presented the financial state of the society. He pointed out that the surplus for 1996 is still in a state of flux, but the 1995 surplus figure of \$673.6K is actual. He then presented the 1997 Proposed Budget, noting that in the conference budget there is a sliding window of expenses and incomes. Discussion took place regarding the Keynote Speakers budget line. Mike Lightner suggested that it be struck from the budget, with the understanding that if divisions go over budget, the members not be parsimonious about it. This suggestion was proposed as a motion and passed unanimously. A motion to approve the entire 1997 proposed budget was then made and passed unanimously.

At this point in the meeting, Michael Lightner requested a change in the agenda since R.W. Liu had to leave for the airport before the meeting adjourned. So, on behalf of the entire Board of Governors, Michael Lightner acknowledged the outstanding life-long contributions of Professor Ruey-wen Liu to the IEEE Circuits and Systems Society, especially during the past three years during which R.W. Liu led the Society in the capacity of president-elect, president and past president. The acknowledgment was received with a round of applause.

Beginning the division reports, Rui de Figueiredo, vice president for publications, presented a topical outline for the Publications Division.

On the status of the periodicals, he reported that Josef Nossek and John Choma, the editors of *T-CAS I* and *II*, respectively, have had interactions on exchange of papers and have come to an agreement on how these papers will be processed. John Choma commented that some authors are vehemently opposed to the exchange. Also, two transactions will have changes in editors. For the *T-VLSI*, Don Bouldin wishes to step down as editor

and Bing Sheu has formally been appointed to take over effective January 1997. Ming-Ting Sun is also ending his term as editor of the *T-CSVT*, having nominated current associate editor, Ya-Qin Zhang, to replace him. Ming-Ting Sun has also made an additional page budget request.

As announced by Michael Lightner, Rui de Figueiredo reiterated that the Electronic Publishing Initiative is proceeding well and Randy Geiger has been extremely active and helpful in this regard.

Rui de Figueiredo then proposed the Fast Electronic Publishing Initiative (FEPI) in place of the previously proposed journal, *CAS Letters*. Some special features of the FEPI are: separate page budget, special fast electronic review procedure, separate paging to permit easy archival referencing, and the common managing editor located at IEEE Headquarters in Piscataway. He also proposed that the \$30,000 which was made available for the *Express Letters* project be used for the FEPI. He emphasized that this is strictly an experimental project. Mike Lightner suggested that this matter be given back to the Publications Division for further discussion.

For the Conference Division, Hari Reddy presented a detailed report on the status of the division which included a conference status report, the projected Conference Division budget for 1997, the ISCAS cycle and site selection, the DAC surplus figures, and the division's plans for the promotion of DAC activities. Concerning the ISCAS cycle, Hari Reddy reported that the Conference Division members have been studying and deliberating the ISCAS cycle for some time now and have finally reached an agreement for a new six-year cycle, which will go into effect starting in the year 2001.

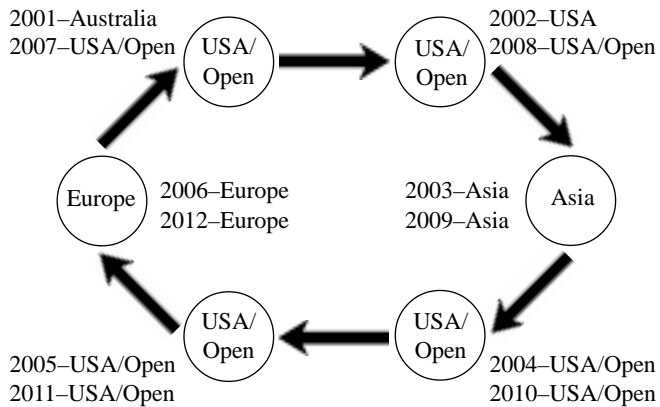
He pointed out that with this new six-year cycle, the traditional European and Asian sites will get ISCAS every six years as at present, and that of the two consecutive USA/Open sites, one will certainly be in the USA. The Conference Division will make sure that in the six-year cycle, USA hosts at least three times and USA may host two years in a row as at present. Michael Lightner suggested that if at any time the conference goes out of the USA (out of the four), the Conference Division should bring the proposal to the BOG. The six year schedule is as follows:

Issues for CAS from TAB

- Should Barbara Wehner have a contract as an independent contractor: typical IEEE procedure?
- Membership on IEEE committees from CAS.
- Can we conduct our own elections?
- Develop our own internal review procedures so Society review becomes trivial.
- Michael Lightner appointed as CAS representative to DAC.
- PUBs Board will begin a separate review of all IEEE publications with time to publication as a main issue.
- IEEE meeting manual will be on the WWW in 1997. All chapters and conferences should be informed of this.
- Conference closings still a concern - we should have a report at each BOG on conference status with regard to closings.
- Set up mail/list servers on our homepage (<http://www.eecs.uic.edu/~cas>) so members can contact officers and board members more easily.
- Nonlinear Dynamics meeting in Moscow sponsored by the new chapter - request for funds \$10K-\$12K - recommend support and splitting between technical and regional budgets.
- Possible joint chapters with the SPS - how to setup and enable. Action for the regional VPs.
- Need representative to Solid-State Circuits Society. Lightner suggests a representative from the Technical Activities Division. Amend constitution for reciprocal representation?
- Need for a much better fiscal structure in the Society.
- Possibility of adding a student member to the BOG to help with ideas addressing needs and views of new members and young engineers.
- Examination of the Eastern European Library project and their desire for CD-ROMS, and use of regional funds to make a successful Region 9 chapter chairs meeting.

... GOVERNORS' CORNER

BOG Report . . . continued from Page 11



On promotion of the DAC activities, Hari Reddy said the division has been looking into a number of items including: promotion of Design Automation activities at ISCAS, funding Design Automation/CAD sessions, tutorials and keynote talks at major society sponsored conferences and symposia, CAS Society sponsored special sessions, tutorials, and keynote talks at the Design Automation Conference, many specialized workshops relating to DA/CAD, promotion of DA activities through the Distinguished Lecturer Program and selection of Distinguished Lecturers from the DA/CAD community, open invitation to DAC/EC and SCC members to at-

tend BOG meetings, and active involvement of the CAS Society in the organization of DAC.

Under Regional Activities, neither Franco Maloberti nor Paulo Diniz were present but had submitted written reports for Regions 8 and 9, respectively. Isao Shirakawa summarized the activities in Region 10 from his written report.

The Technical Activities report was also in written form due to the absence of Chris Toumazou, vice president for technical activities.

In the final report, John Choma, president-elect, briefly mentioned some of the areas he will be looking into as president next year. These include seeing the society take a more proactive stance on education, revisiting the Long Range Planning Committee (LRPC) set up by Ruey-wen Liu, and renaming the two *T-CAS* journals.

There was no old business, and under new business it was pointed out that due to an oversight, the budget for *T-VLSI* was omitted in the 1997 budget. A request of \$20,000 for *T-VLSI* will be submitted for BOG approval at the next meeting. That next meeting will be held on Sunday, June 8, 1997, in Hong Kong prior to ISCAS.

Before adjournment, Michael Lightner thanked all members of the Executive Committee and the Board of Governors for their very valuable involvement in the Circuits and Systems Society this year, and Barbara Wehner, the new CAS administrative assistant for all of her help during the past year.

CAS Constitution and Bylaws Changes

Three amendments were approved by the Board of Governors at their meeting on November 10, 1996. These changes will take effect 30 days after publication (i.e. the mailing of this issue) unless two percent or more of the members object.

1. Amend Article IV, Section 1 of the Constitution to clarify membership of the Executive Committee on Board of Governors.

Old: *The Society shall be managed by an administrative committee known as the Board of Governors, consisting of fifteen voting members who are elected from the membership at large. In addition, the following officers are ex officio members with vote, whether or not they are also included in the fifteen: President, President-Elect, Administrative Vice President, one or more Vice Presidents as specified in the Bylaws, Immediate Past President.*

Amended: The Society shall be managed by an administrative committee known as the Board of Governors, consisting of fifteen members who are elected from the membership at large and members of the Executive Committee detailed in the Bylaws.

2. Amend Article V, Section 1 of the Bylaws to add regional vice presidents to the Executive Committee.

Old: . . . *In addition to the President, the Executive Committee members are: President-Elect, Past President, Administrative Vice President, Vice President-Technical Activities, Vice President-Conferences, Vice President-Publications.*

Amended by adding: Vice President-Region 8, Vice President-Region 9, Vice President-Region 10.

3. Amend Article VI, Section 1 of the Bylaws for better coordination of the awards process.

Old: *Awards, as authorized by the Board of Governors with the approval of the TAB Awards and Recognitions Committee, shall be given by the Society. Each award shall be assigned to the appropriate Awards Committee. The committees shall solicit nominations throughout the society for the awards, screen candidates, and recommend awards to the Board of Governors. Where appropriate, the committees should also recommend candidates for Medals, Awards, and other recognitions within the IEEE. The following awards committees shall be appointed by the President: 1. Achievement awards committee 2. Education awards committee 3. Prize paper awards committee 4. Service awards committee.*

Amended: Awards, as authorized by the Board of Governors with the approval of the TAB Awards and Recognitions Committee, shall be given by the Society. The awards shall be recommended by the Awards Committee, which is appointed by the President and chaired by the Past President who coordinates all awards activities. The committee shall solicit nominations throughout the society for the awards, screen candidates, and recommend awards to the Board of Governors. Where appropriate, the committee should also recommend candidates for Medals, Awards, and other recognitions within the IEEE. The following awards subcommittees shall be appointed by the President: 1. Achievement awards subcommittee 2. Education awards subcommittee 3. Prize paper awards subcommittee 4. Service awards subcommittee.

CALL FOR PARTICIPATION

1997 IEEE International Symposium on Circuits and Systems

Circuits and Systems in the Information Age

June 9–12, 1997

Hong Kong Convention & Exhibition Centre
Hong Kong

ISCAS'97



IEEE Circuits & Systems Society

Invitation

1997 IEEE International Symposium on Circuits and Systems (ISCAS'97) will be the 30th in the series of annual international conferences sponsored by the IEEE Circuits and Systems Society. It will include regular sessions of about 630 papers, an acceptance rate of about 62%, on all aspects of 8 technical tracks as well as special sessions of 83 papers on specific advanced topics of circuits and systems. About half of the papers will be presented in poster sessions, which give the audience greater interaction with the presenters. ISCAS is one of the largest conferences and activities of the IEEE Circuits and Systems Society. It is anticipated that more than one thousand people from research institutes, academics, and industries all around the world will attend the conference.

Hong Kong is one of the most dynamic cities in the world with world-class facilities, easy accessibility, exciting entertainment, high levels of services, professionalism and industries. Since Hong Kong will become the Special Administrative Region of PR China on July 1, 1997, many visitors are interested to travel to Hong Kong in June/July 1997. Therefore, we advise you to make your airline reservation as early as possible to attend ISCAS'97.

Technical Tracks of Regular Sessions

1. *Analog Circuit & Signal Processing*
A/D & D/A Conversions, High-frequency Circuits;
Analog Circuits & Techniques, Wavelets
2. *Circuit Theory & Power Systems*
Linear & Nonlinear Circuit, System & Theory;
Distributed Networks; Power Electronics & Systems
3. *Communication & Multimedia*
High Definition TV; Image Processing;
Video & Multimedia Technology; Visual Communication;
Wireless Communication Circuits; Opto-electronic Circuits
4. *Computer-Aided Design*
Modeling and Simulation; Large-Scale Networks;
Optimization Methods
5. *Digital Signal Processing*
DSP & Applications; Digital Filters; Speech & Audio
Processing; High Speed Modems; Adaptive Signal Processing
6. *Neural Systems*
Neural Networks; Fuzzy Logic & Circuits
7. *VLSI*
Analog and Digital ICs; Parallel Architectures; Low
Voltage / Low Power ICs
8. *Industrial Applications*
Testing; Fault Analysis & Fault Tolerant Systems;
Sensors; ATM Switch Design; Realization of
Speech & Video Compression

Half-day Short Courses

1. MPEG-4 Video Coding Standard
2. Design for Test of Analog and Mixed-Signal Circuits
3. High Performance VLSI Signal and Video Processors
4. Multimedia Technologies and Applications
5. High Speed VLSI Interconnects
6. Circuit Simulation Based on Wavelet Method

Special Sessions

1. Learning on Silicon
2. Advanced VLBR Video Coding
3. Applications of Chaos in Communications
4. VLSI Systems Design for High-Bit Rate Communications
5. Digital VLSI Compatible Analog Design
6. Timing and Low-power Methodologies for DSP synthesis
7. Applications of Microelectromechanical Systems
8. VLSI Neural Networks for Intelligent Signal Processing
9. Networked Multimedia Technology and Applications
10. Instantaneous Companding in Analog Signal Processing:
Circuits and Synthesis
11. Multidimensional Signal Processing and Cellular Neural
Networks

Hotel and Travel Information

The Organizing Committee has appointed Morning Star Travel Service Ltd as a local agent for accommodation arrangement. Please see our web site for details.

Registration Fees

	Payment received on/before May 1, 1997	Payment received after May 1, 1997
IEEE member	US\$420 or HK\$3,200	US\$470 or HK\$3,600
Non-member	US\$480 or HK\$3,700	US\$530 or HK\$4,100
Full-time student	US\$120 or HK\$900	US\$145 or HK\$1,100

Any inquiries can be directed to
ISCAS'97 Conference Secretariat

Dept. of Electrical and Electronic Eng.
University of Hong Kong, Pokfulam Road, Hong Kong
Tel: (852) 28592710 Fax: (852) 25598738
E-mail: iscas97@hkueee.hku.hk
Web site: <http://www.eee.hku.hk/~iscas97/>

34th Design Automation Conference



Anaheim

June 9 - 13, 1997

Anaheim Convention Center
Anaheim, California

DAC is the premier conference devoted solely to the field of Design Automation. It is the conference where the Electronic Design Automation industry exhibits leading edge technology to enable the design of circuits and electronic systems to fuel the information age.

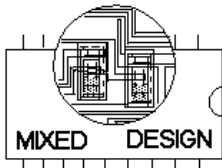
Conference Manager:

P.O. Pistilli
MP Associates, Inc.
5305 Spine Rd., Ste. A
Boulder, CO 80301
(303) 530-4562
pat@dac.com

For up-to-date information
visit the DAC Web Site at:

<http://www.dac.com>

ISCAS/DAC



MIXDES'97

Please consult our WWW site:
<http://www.dmcsp.lodz.pl/mixdes97.html>

4th International Workshop
MIXED DESIGN OF INTEGRATED
CIRCUITS AND SYSTEMS
EDUCATION OF COMPUTER AIDED DESIGN
OF MODERN DEVICES AND ICs
Poznan, POLAND, June 12-14, 1997

Coordinating Organizer and Contact Person:
Dr. Zygmunt CIOTA, Technical University of Lodz
Department of Microelectronics and Computer Science
Al. Politechniki 11, 93-590 Lodz, POLAND
Tel: (+48-42) 31 26 45; Fax: (+48-42) 31 26 28
E-mail: MIXDES97@DMCS.PLODZ.PL

≈ ≈ CALL FOR PAPERS ≈ ≈
The International Conference on Microelectronics
October 8-10, 1997 Jakarta, Indonesia

Deadline for paper submission: May 1, 1997
For Europe and North America: Prof. M.I. Elmasry
Director, VLSI Research Group
ECE Department
University of Waterloo
Waterloo, ON, N2L 3G1, CANADA
Phone: (519) 888-4567, Ext. 3753
Fax: (519) 746-5195
E-mail: elmasry@vlsi.uwaterloo.ca
For other areas: Dr. Onno W. Purbo
Inter University Center on Microelectronics
Institute of Technology Bandung
Jl. Ganesha 10, Bandung 40132, Indonesia
Phone: (62)-22-2506280, (62)-22-2508763
Fax: (62)-22-2508763, (62)-22-771153
E-mail: onno@kalpataru.netura.net.id
ycldav@cng.itb.ac.id
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/// // CALL FOR LATE NEWS PAPERS // //

1997 BIPOLAR/BiCMOS CIRCUITS AND TECHNOLOGY MEETING
Marriott City Center Hotel — Minneapolis, MN
Short Course: Sept. 28, 1997, Conference: Sept. 29-30, 1997

A very limited number of late news papers will be accepted. Authors are asked to submit late news abstracts and summaries describing only recent developments. Late news should be based on new data and findings since the March 31, 1997, regular paper submission deadline.

DEADLINES: Monday, June 30, 1997 - - - - Receipt of abstract and summary
Monday, July 28, 1997 - - - - Notification of acceptance

All questions or inquiries for further information should be directed to:
Conference Manager, Janice Jopke, CCS Associates, 6611 Countryside Drive, Eden Prairie, MN 55346. Tel: (612) 934-5082; Fax: (612) 934-6741; E-mail: jjopke@aol.com

3rd IEEE International
Mixed Signal Testing
Workshop

Port Ludlow Conference Center
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June 3 - 6, 1997



This Workshop explores state-of-the-art test concepts and trends in the area of analog and mixed signal circuits.

For technical program information, please contact either of the Technical Program Co-Chairs:

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University of Twente
Department of Electrical Engineering
P.O. Box 217
7500 AE Enschede, The Netherlands
Phone: +31 53 4892646, Fax: +31 53 4892799
E-mail: hans@ice.el.utwente.nl

Stephen Sunter
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1525 Carling Ave., Suite 404
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Phone: +1 613 722 0854, Fax: +1 613 722 4164
E-mail: sunter@lvision.com

For general information, please contact:

Mani Soma
E-mail: soma@ee.washington.edu

or

Pamela Eisenheim, Organization Assistant
Department of Electrical Engineering
University of Washington
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Seattle, WA 98195-2500 USA
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Web site:

<http://www.ee.washington.edu/mad/imst97.html>

1997 IEEE Workshop on SiGNAL PROCESSING SYSTEMS (SiPS)

Design and Implementation

formerly IEEE Workshop on VLSI Signal Processing

An Annual Activity of the :

IEEE Signal Processing Society Technical Committee on the

Design and Implementation of Signal Processing Systems

and now jointly co-sponsored for the first time by

IEEE Circuits and Systems Society Technical Committee on

VLSI Systems and Applications

In cooperation with

Circuits and Systems Society Technical Committee on **Multimedia Systems and Applications**

Signal Processing Society Technical Committee on **Multimedia Signal Processing**

November 3-5, De Montfort University, Leicester, UK

The objective of this workshop is to provide a forum for discussion of new developments in the design and implementation of Signal Processing Systems including *VLSI and Systems Technology, DSP Technology, and Applications*. A hard-bound proceedings of the workshop will be published.

Workshop Theme: "Multimedia Systems on a Chip"

For more information on the schedule (deadline for paper submission is April 1, 1997) and other details for the Call For Papers please visit our web site at <http://www.eng.dmu.ac.uk/~mki/sips97/> or contact the general chair **Mohammad Ibrahim** (email: mki@dmu.ac.uk).



IEEE

SiPS
97



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European Conference
on Circuit Theory
and Design

ECCTD'97

30 August — 3 September 1997 Budapest, Hungary

Prof. Dr. Tamás Roska
ECCTD'97 Technical Program Chairman
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Hungary
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Fax: + 36-1-269 8264
E-mail: ecctd97.tech@mmt.bme.hu

Ms. Zsuzsa Somlyódy, ECCTD'97 Secretary
Ms. Mónika Jetzin
TRIVENT Conference Office
H-1125 Budapest, Szamóca u. 6/b, Hungary
Phone/Fax: +36-1-156 6240
E-mail: ecctd97.sec@mmt.bme.hu

Further information: E-mail: ecctd97@mmt.bme.hu
WWW page: <http://www.mmt.bme.hu/ecctd97>

International Conference on Neural Networks



Westin Galleria Hotel, Houston, Texas, USA

Tutorials: June 8, Conference: June 9-12, 1997

This conference is a major international forum for researchers, practitioners and policy makers interested in natural and artificial neural networks.

Conference Secretariat:

Meeting Management

2603 Main Street, #690, Irvine, CA 92714

Phone: (714) 752-8205; Fax: (714) 752-7444

E-mail: Meeting Mgt@aol.com

Carol at Meeting Management will answer questions about ICNN97.

Web Site: <http://www.mindspring.com/~pci-inc/ICNN97>

==== **CALL FOR PAPERS** ====

N I C R O S P ' 9 7

1997 International Workshop on Neural Networks
for Identification, Control, Robotics, and Signal/Image Processing
Act-City Hamamatsu October 6-8, 1997 Hamamatsu, Japan
Convention Center

Deadline for Submission: **March 31, 1997**
Acceptance Notification: **June 1, 1997**
Camera-ready manuscripts due: **July 31, 1997**

For further information, contact:
Prof. Kenzo Watanabe
Research Institute of Electronics, Shizuoka University
3-5-1 Johoku, Hamamatsu 432 Japan
Tel: +81 (53) 478-1326; Fax: +81 (53) 478-1326
E-mail: watanabe-k@rie.shizuoka.ac.jp



General Chair: A. B. Kahng
Program Chair: M. Sarrafzadeh

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This year's inaugural Symposium focuses on the challenges of high-performance deep-submicron design, as well as the necessary interactions between physical design and higher-level synthesis tasks.

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Embassy Suites at Napa Valley
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April 14-16, 1997

Symposium Information:

To obtain information regarding the Symposium or to be added to the Symposium mailing list, please send e-mail to ispd97@cs.virginia.edu.

Information can also be found on the ISPD-97 web page: <http://www.cs.virginia.edu/~ispd97/>

40th Midwest Symposium on Circuits and Systems

Hyatt-Regency Hotel, Sacramento, CA
August 3-6, 1997

Please address all correspondence to the particular chair at:
40th Midwest Symposium on Circuits and Systems
Electrical and Computer Engineering
University of California
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Telephone: (916) 754-6216
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Local Arrangements:

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Administration & Exhibit Information:

Sharon Baumgartner
University of California, Davis
MWSCAS-97 (mwscas97@ece.ucdavis.edu)
Tel: (916) 754-6216; Fax: (916) 752-8428

Visit our Web page at:

<http://www.mwscas.org/Symp97/homepage.html>

EP'97/ICCEC'97

IEEE International Conference on
Evolutionary Computation
EPS Evolutionary Programming Conference

Indianapolis, Indiana, U.S.A.
April 13-16, 1997

Sponsored by:

IEEE Neural Networks Council and the
Evolutionary Programming Society

For information contact:

Russ Eberhart — — — — —
Purdue School of Engineering and Technology
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Phone: 317-278-0255
http://www.engr.iupui.edu/et/ieec_con.htm

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CALL FOR PARTICIPATION NON-LINEAR DYNAMICS OF ELECTRONIC SYSTEMS

N D E S ' 9 7

An International Specialist Workshop
Moscow, Russia

June 26 - 27, 1997

The fifth annual workshop will be hosted by the Moscow Technical University of Communications and Informatics and Institute of Radio Engineering and Electronics. The workshop will address theoretical and practical issues in non-linear electronic devices, circuits and systems, with an emphasis on dynamic behavior, chaos and complexity.

For further information:

Dr. Dmitry Makarhtikov
Moscow Technical University of Communications and Informatics
Aviamotornaya St., 8a, Moscow 111024, Russia
E-mail: dmitr@chaos.msk.su
phone: (095) 273-75-09; fax: (095) 274-00-32, (095) 273-17-13

1997 IEEE Workshop on Nonlinear Signal and Image Processing

September 7-11, 1997

GRAND HOTEL

Mackinac Island, Michigan

For further information:

Prof. Edward J. Coyle
School of Electrical and
Computer Engineering
Purdue University
1285 EE Building
West Lafayette, IN 47907-1285
tel: 317/494-3470
fax 317/494-3358
email: coyle@ecn.purdue.edu.

Workshop information is available at:

<http://www.ecn.purdue.edu/NSIP>

FIRST CALL FOR PAPERS

**1997 International Symposium on
Nonlinear Theory and its Applications**
N O L T A ' 9 7
Hilton Hawaiian Village, Hawaii
Nov. 29 - Dec. 3, 1997

Technical Program chairman:

Prof. Mamoru Tanaka
EEE Dept., Sophia Univ.
7-1, Kioi-cho, Chiyoda-ku Tokyo
102 Japan
Fax: +81-3-3238-3321
E-mail: nolta97@mamoru.ee.sophia.ac.jp
E-mail submissions are strongly recommended.

Authors' Schedule

Deadline for submission of summaries:

July 19, 1997

Notification of acceptance:

August 30, 1997

Deadline for 4 pages camera-ready
manuscript: **October 18, 1997**

Proposals for special sessions and workshops
may be submitted to Prof. M.Tanaka on before
July 19, 1997.

For up-to-date information:

<http://www.tlab.ee.sophia.ac.jp/nolta97/>

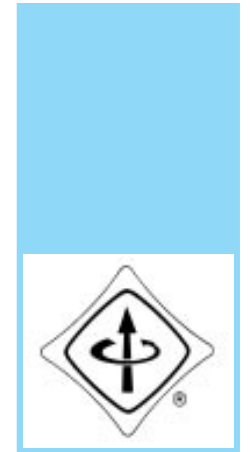


CALL FOR NOMINATIONS

Board of Governors

Each year, 5 members of the CAS Society are elected to the Board of Governors for 3-year terms. The Board of Governors shall represent the members of the Society and approve the Society's annual budget, amendments to the Constitution and Bylaws, and authorize the expenditure of Society funds. Members of the Board of Governors should not miss annual meetings (at ISCAS in May/June and ICCAD in November) more than 2 times consecutively. Nominations from the Society membership must include at least 15 signatures of Society members, excluding students. Upon receipt of nominations, the Nominations Committee will submit at least 11 candidates for Society-wide election of the five Board members.

If you wish to nominate a member to the Board of Governors, then, after obtaining the consent of the nominee to serve if elected, please fax the name of the nominee, address, telephone, fax number, and e-mail address, if available, to the CAS Nominations Committee Chair on the form below. Board of Governors nominations must be received by **June 1, 1997**. Send nominations to Professor Ruey-wen Liu at the address on the form below.



Editor: Dr. Michael K. Sain
Electrical Engineering
University of Notre Dame
Notre Dame, IN 46556 USA

President's Message. . .continued from Page 5

Although we collectively define the present state of the Circuits and Systems Society, the future lies in the hands of a relative youth whose creativity and leadership must be aggressively recruited. How best can we recruit and engage the societal participation of younger members in both the academic and industrial sectors of our technical community? How best can we encourage students to join and engage in societal functions?

Society Officers

Each year, members of the Board of Governors elect President-Elect, Vice Presidents for Administration, Publications, Technical Activities, Conferences, Region 8, Region 9, and Region 10. The CAS Bylaws have provision for nominations from Society members by written petition with at least 15 members' signatures, excluding students.

If you wish to nominate a member to any of the above CAS offices, then, after obtaining the consent of the nominee to serve if elected, please fax the name of the nominee, address, telephone, fax number, and e-mail address, if available, to the CAS Nominations Committee Chair on the form below. Officer nominations must be received by **September 1, 1997**. Send nominations to Professor Ruey-wen Liu at the address on the form to the right.

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CAS NOMINATION FORM	
CAS Office:	
<input type="checkbox"/> Member of the Board of Governors	<input type="checkbox"/> President-Elect
<input type="checkbox"/> Vice President, Administration	<input type="checkbox"/> Vice President, Conferences
<input type="checkbox"/> Vice President, Publications	<input type="checkbox"/> Vice President, Technical Activities
<input type="checkbox"/> Vice President, Region 8	<input type="checkbox"/> Vice President, Region 9
<input type="checkbox"/> Vice President, Region 10	<input type="checkbox"/> The Candidate has agreed to serve if elected; and 15 valid names and addresses (mail, phone, fax, email), with signatures, are attached!
Candidate:	
Name _____	
Address _____	
Phone _____	Fax _____
Nominator:	
Name _____	
Address _____	
Phone _____	Fax _____
<p><i>Please mail or fax this form to:</i> Ruey-wen Liu, CAS Nominations Committee Dept. of Electrical Engineering University of Notre Dame Notre Dame, IN 46556 Tel. (219) 631-6228 Fax (219) 631-4393</p>	