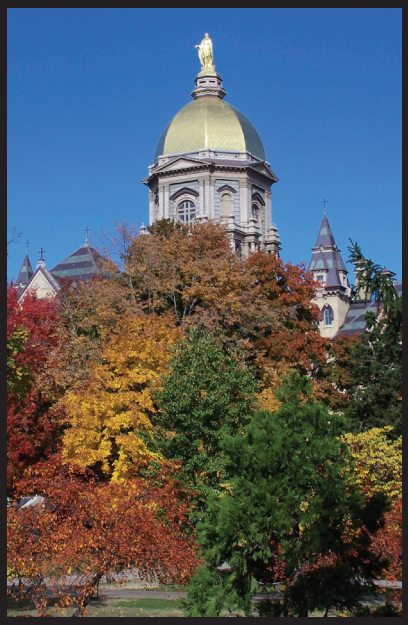

Architectures *for* Post-CMOS Switches

ONE-DAY WORKSHOP

This event will examine how emerging post-CMOS switches studied in the Nanoelectronics Research Initiative can be used most effectively in design – a timely topic given the profound challenges of scaling conventional CMOS. By exploring alternatives to Boolean logic expressed in conventional static combinatorial CMOS circuits, the workshop will motivate and inspire researchers to identify logic functions accelerated in hardware by idiosyncrasy in specific post-CMOS devices. New switch architectures may solve problems that CMOS either cannot address or attempts to do in a most cumbersome way. The workshop will also include an interim read-out of the NRI emerging switches architecture benchmarking effort.

Speakers include:

Ralph Linsker (IBM)
Ken Shepard (Columbia)
Larry Cooper (Arizona State)
Andreas Andreou (Johns Hopkins)
David Ricketts (Carnegie Mellon)
Tamas Roska (Hungarian Academy of Sciences)
Azad Naeemi (Georgia Tech)
André DeHon (Univ. of Pennsylvania)
Greg Snider (Hewlett-Packard)
Elad Alon (Univ. of California-Berkeley)
Dan Hammerstrom (Portland State)

Tuesday, August 18, 2009

University of Notre Dame • McKenna Hall

8:00 a.m. - 6:00 p.m., followed by dinner in the Notre Dame Stadium Press Box

Registration and hotel block: http://mind.nd.edu/
Workshop organizers: Wolfgang Porod, University of Notre Dame
Kerry Bernstein, Applied Research Associates
More information: Heidi Deethardt at 574-631-0279 or deethardt.1@nd.edu

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