

Curriculum Vitae

Mehrdad Aliasgari

Address:

29 Fischer Grad. Res.
Apt. # 2A
Notre Dame, 46556
IN, USA

Email: maliasga@nd.edu
Cellphone: (574) 520-0864
Homepage: <http://www.nd.edu/~maliasga/>

Personal Information

Marital Status: Single
Date of Birth: April 8 1985

Education

University of Notre Dame (2008-present)
Ph.D Student in Computer Science and Engineering
Research Area: Applied Cryptography
Advisor: [Dr.Blanton](#)
GPA: 3.867
Sharif University of Technology (2003-2008)
B.Sc in Electrical Engineering - Communications
GPA: 16.18 out of 20

Research Interests

Applied Cryptography
Secure Outsourcing and Secure Multiparty Computation
Security in Biometrics
Data Communication Network Security and Wireless Networks
Security in Digital Signal Processing and Watermarking

Technical Report

M. Blanton and M. Aliasgari, [Secure Computation of Biometric Matching](#),
CSE Technical Report TR 2009-03, University of Notre Dame, Apr. 2009.

Thesis

B.Sc: WiMAX SECURITY (under supervision of Dr. vosoughi vahdat)

Presentation

§ “An Analysis of Shared-Memory Models in Multiprocessor Systems Using

Matrix Applications”, University of Notre Dame, April 2009.

§ “Partial Authentication in Balancing Traceability and Anonymity”,
University of Notre Dame, December 2008.

§ “A new public key cryptosystem based on discrete root of m-th degree
modulo n” Sharif University of Technology, January 2008

Honors & Awards

- Ranked **30** out of 500,000 Participants of the nationwide university entrance exam (2003)
- Recipient of the best poster award by the faculty at the 5th Annual CSE Student Research Symposium, November 2010.
- Recipient of an award for high rank in nationwide university entrance exam from the Minister of Science, Research and Technology (2003)
- Recipient of educational diploma on GSM (focus on Signaling) from GSM college Of MCI (mobile communication institute) under Control of Minister of ICT (Information And Communications Technology)

Selected Projects

- An Evaluation of Scalability, Performance and Reliability of a Distributed Operating System in an Image Comparison Application using Hadoop. Fall 2009
- Partial Authentication in Balancing Traceability and Anonymity, Fall 2008
- Developing an NC (Norton Commander) both server side and client side with full permission (upload and download). Spring 2007
- Design and Implementation of DDS (Direct Digital Synthesizer) in 1Khz-100Khz by step 1KHZ using AT89C51AC2 microcontroller. Spring 2007
- Simulation of Transients on Transmission lines with arbitrary termination and make animation of wave propagation by solving ODE in time domain. Fall 2006
- Simulation of TCP Vegas and TCP Reno using the NS2. Fall 2006
- Developing an AM Modulator and Demodulator by bipolar ICs. Spring 2006
- Design of an ATM (Automatic Teller Machine) based on 80x86 processor and its peripherals. Spring 2006
- Design of a Digital Electricity Meter using AT89C51AC2 microcontroller. Fall 2005
- Designed a high gain, high power amplifier with near Line-to-Line output voltage swing and high input common mode swing. Fall 2005
- Design of a Simple 8-bit Processor for implementing on FPGA, writing the code in Verilog HDL, simulating and testing it with Quartus II software. Spring 2005
- Simulation of the magnetic field of multi magnetic dipoles located

arbitrarily on space and also their forces and torques by using the numerical Method of Moments. Fall 2004

- mentored students in signal processing projects(using signal processing toolbox of MATLAB). Fall 2005-Present.

Computer and Programming Proficiency

- **Programming**

- C/C++

- PHP

- ASP

- Javascript

- VBscript

- XML/XSL

- HTML

- Socket programming

- Verilog HDL

- Assembly 8051 and 80x86

- **Engineering Softwares**

- MATLAB and SIMULINK

- OrCAD

- Quartus II

- MWO

- NS-2