
EE566 Solid State Devices

Spring 2004

Dept of Electrical Engineering

University of Notre Dame

Instructor: Debdeep Jena (djena@nd.edu, x8835, 200 Cushing)

Course Information

Website

Most of the important information (announcements, assignments, and printed notes) will be posted on the class website – so make sure you check it regularly - <http://www.nd.edu/~djena/ee566.htm>

Office Hours

Official: M W 10:30 – 11:30

Unofficial: Whenever I am in the office

Text

The required text for the class is –

- Physics of Semiconductor Devices (Michael Shur, Prentice Hall)

The suggested references are –

- Fundamentals of VLSI devices (Taur and Ning, Cambridge University Press)
- Physics of Semiconductor Devices (Simon Sze, Wiley)

Topics

- Review of semiconductor physics
- p-n junctions, heterojunctions, Schottky contacts, Ohmic contacts
- BJTs (including HBTs)
- FETs (MOSFETs, MESFETs, JFETs, MODFETs)
- LEDs

Grading

Assignments (one every week)	25%
1 st Mid Term	20%
2 nd Mid Term	25%
Final Exam	30%

Assignments

1/week. *Typically* one problem per assignment.